

1.	Name	Dr. SANDEEP SINGH GILL 
2.	Designation:	Professor and Head (ECE)
3.	Department	Electronics and Communication Engineering
4.	Date of Joining	1 May 1998
5.	Qualifications:	<ol style="list-style-type: none"> 1. Ph.D (Electronics and communication engineering)[NIT, Hamirpur] 2. M.Tech(Electronics & Comm. Engineering)[GNDEC,Ludhiana 2005] 3. MBA(Marketing /Systems & Control)[PAU, Ludhiana 1994] 4. B.E(Electronics & Comm. Engineering)[Punjab university, Chandigarh 1991]
6.	Experience	<ol style="list-style-type: none"> 1. Head of Department (May,2014 onwards) 2. Professor (18 February, 2013 onwards) 3. Associate/ Assistant Prof.(G.N.D.E.C.,Ludhana)[16August, 2005 onward] 4. Lecturer (G.N.D.E.C.,Ludhana)[1 May 1998 till 15 August 2005] 5. Centre Manager(Tulec Computer Education, Khanna)[March 1996-April 1998] 6. Senior Executive(Wipro Ltd., Jalna Road, Aurangabad)[Jan 1994 to Dec. 1995]
7.	Paper published	National Journals: 03 International Journals: 24
8.	Paper presented in conferences	Conferences: 80
9.	PhDs/ M.Tech thesis guided	Master's: 60 PhD: 06(Guiding)
10.	Books published/IPRs./	<ol style="list-style-type: none"> 1. "Handbook of Management", PragatiPrakashan, 2005. 2. Contributed essay on " Marketing in 21st century", in book "

	Patents	BusinessManagement- Parameters & Perspectives”, edited by Dr. Ajay Prasher& RajeevPrasher, Medallion Press,2006. 3. Proceedings, Recent Advances in Electronics & CommunicationTechnologies,2013 4. Proceedings, International Conference on Soft Computing Applications in Wireless Communicaton, 2017 5. “CMOS SRAM Design and analysis of low leakage and high speed SRAM cell” Co-authored by Navneet Kaur and Rohin Gupta by Lambert Academic Publishers
11.	Professional Memberships	Member, Indian Society for Technical Education (ISTE) Member, Ludhiana Management Association Fellow, Institution of Electronics & Telecommunication Engineers(IETE) Member, Insititute of Electrical and Electronics Engineers (IEEE)
12.	Area of Specialization/Consultancy Activities	VLSI Design Automation, Soft Computing and CAD Tools
13.	Major Grant Fetched	1. Opto electronics and fiber optics projects (TAPTEC) 2. Upgradation of EDA Lab (MODROBS) 3. DST,PTU,CSIR Grants for conduct for conferences etc. 4. As Departmental Coordinator Consultancy conducted 6 weeks courses on “Circuit design using VHDL” and generated resources through training students of other colleges and AMIE. 5. AICTE sponsored FDP on Advanced VLSI Design Techniques 2013-14 6. AICTE/ISTE sponsored FDP on “ Circuit Design using VHDL” 2003 7. CSIR, DRDO sponsored , International Conference on Soft Computing Applications in Wireless Communicaton, 2017
14.	Interaction with professional institutions	1.As expert in selection committees, conferences, STTPS, and in collaborated research. 2. Member board of studies. 3. Reviewer in reputed journals and conferences. 4. Examiner at Ph.D / PG Level