

8. Consultancy

Session (July, 2013 - June, 2014)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1.	Dr. Sandeep Singh Gill	Electronic Circuit Design using VHDL	Private	Consultancy	75,000	6 weeks	July, 2013	Completed
2.	Narwant Singh							
3.	Harminder Kaur							
4.	Gurjot Kaur							
5.	Gurpurneet Kaur							
6.	Rohin Gupta							
7.	Narwant Singh							
8.	Balwinder Singh Dhaliwal							
9.	Dr. Munish Rattan							

Session (July, 2014- June, 2015)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1.	Narwant Singh	Electronic Circuit Design using VHDL	Private	Consultancy	32,247	6 weeks	July, 2015	Completed
2.	Dr. Munish Rattan							
3.	Harminder Kaur							
4.	Gurjot Kaur Walia							
5.	Navneet Kaur							
6.	Chahat Jain							
7.	Kuldeepak Singh							
8.	Jasmeet Kaur							
9.	Sapanjot Kaur							
10.	Simranjit Kaur							

Session (July, 2015- June, 2016)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1.	Dr. Sandeep Singh Gill	Electronic Circuit Design using VHDL	Private	Consultancy	39,326	6 weeks	July, 2015	Completed
2.	Narwant Singh							
3.	Gurjot Kaur Walia							
4.	Navneet Kaur							
5.	Naazia Makkar							
6.	Dr. Baljeet Kaur	Optical System Design						
7.	Kuldeepak Singh							
8.	Daljit Singh							
9.	Sapanjot Kaur							

Session (July, 2016- June, 2017)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1.	Dr. Sandeep Singh Gill	Digital Circuit Design using VHDL	Private	Consultancy	2,51,100	Six weeks	July, 2016	Completed
2.	Narwant Singh							
3.	Harminder Kaur							
4.	Jasmeet Kaur							
5.	Simranjit Kaur							
6.	Naazia Makkar							
7.	Dr. Munish Rattan	Embedded System using Arduino						
8.	Gurjot Kaur Walia							
9.	Harminder Kaur Aulakh							
10.	Jaspreet Kaur							
11.	Kirandeep Kaur							

12.	Dr. Baljit Kaur	Optical System using Optisystem						
13.	Kuldeepak Singh							
14.	Daljit Singh							
15.	Sapanjot Kaur							
16.	Nishu Kansal							

Session (July, 2017- June, 2018)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1.	Dr. Sandeep Singh Gill	Digital Circuit Design using VHDL	Private	Consultancy	2,83,502	Six weeks	April, 2017-July, 2017	Complete
2.	Narwant Singh							
3.	Jasmeet Kaur							
4.	Simranjit Kaur							
5.	Naazia Makkar							
6.	Dr. Balwinder Singh Dhaliwal	Embedded System using Arduino and Texas Instruments Tools						
7.	Jaspreet Kaur							
8.	Daljit Singh							
9.	Dr. Munish Rattan	PCB Design and Fabrication						
10.	Chahat Jain							
11.	Palwinder Kaur							
12.	Tarandeep Singh							
13.	Gurjot Kaur Walia	Advanced Communication Systems	Private	Consultancy		Six months	November, 2017	
14.	Harminder Kaur Aulakh							
15.	Kuldeepak Singh							
16.	Sapanjot Kaur							
17.	Naazia Makkar							