

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

	D4ECA	D3ECA	D3ECB	D2ECA	D2ECB	D1EC1	D1EC2	D1EC3	M1 EC	M2 EC			
M o n d a y	08:30	D4ECA1 D4ECA2 WSN_DE_IV MC_DE_IV -L -L HK PP F105 MW/L	D3ECA WMCS -L BK F106	D3ECB1 D3ECB2 CN_DE_II ITC_DE_II -L -L KDS DS PROJ/L F107	D2ECA AC -L GS G6	D2ECB AC -L SNJ G7	---	---	---	---	---	08:30	
	09:30	OC -L NS F105	D3ECA1 D3ECA2 MWR -P MCES -P SK, PP GJK MW/L MP/L	MCES -L NM F107	D2ECA1 D2ECA2 M&C/L - ISP/L -P P KDS SMM EDC/L EDA/L	OOP&DS -L HLK G7	---	---	---	AME_PE_4 -L MR G8	Thesis BK, HK FO&R/L	09:30	
	10:30	D4ECA1 D4ECA2 VLSID -P OC -P HLK NS COMP/L COMM/L	D3ECA1 D3ECA2 MWR -P MCES -P SK, PP GJK MW/L MP/L	DCS -L GPK F107	D2ECA1 D2ECA2 M&C/L - ISP/L -P P KDS SMM EDC/L EDA/L	D2ECB1 D2ECB2 LCS -T AC -T TDS SNJ TI/L G7	---	---	---	AWMC_PE_3 -L HSG G8	---	10:30	
	11:30	D4ECA1 D4ECA2 VLSID -P OC -P HLK NS COMP/L COMM/L	-x-	-x-	D2ECA1 D2ECA2 AC -T LCS -T GS TDS G8 EDC/L	EMFT -L CJ G7	---	---	---	-x-	---	11:30	
	12:30	-x-	D3ECA1 D3ECA2 MCES -T WMCS -T GJK BK MP/L EDA/L	IS/RC - --	-x-	-x-	PPS LAB - RK COMP/L	---	---	AOCS -P HSG FO&R/L	---	12:30	
	13:30	D4ECA1 D4ECA2 OC -T VLSID -T NS GJK F105 TI/L	D3ECA1 D3ECA2 CN_DE_II ITC_DE_II -LCT -LCT KDS DS MP/L EDA/L	LCS -L TDS G6	IS/RC -	---	---	---	---	13:30			
	14:30	D4ECA1 D4ECA2 SC_DE_III COA_DE_III -L -L GPK NM F105 PROJ/L	D3ECA1 D3ECA2 MC - MC - NS MR G7 COMM/L	D3ECB1 D3ECB2 MC - MC - DS SK MW/L MP/L	IS/RC - ---	D2ECB1 D2ECB2 OOP&DS - AC -P P CJ SNJ COMP/L EDC/L	---	---	---	ADSP -L NK G8	---	14:30	
	15:30	IS/RC - --	OE_1_D3 - OE_T1 OE_ROOM-1	OE_2_D3 - OE_T2 OE_ROOM_2	---	D2ECB1 D2ECB2 OOP&DS - AC -P P CJ SNJ COMP/L EDC/L	---	---	---	---	---	15:30	

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

		D4ECA	D3ECA	D3ECB	D2ECA	D2ECB	D1EC1	D1EC2	D1EC3	M1 EC	M2 EC		
Tuesday	08:30	D4ECA1 D4ECA2 SC_DE_III COA_DE_III -L -L GPK NM F105 MP/L	MCES -L GJK F106	MWR -L SK F107	IM&DA -L SMM G6	EMFT -L CJ G7	---	---	---	AWMC_PE_3 -L HSG G8	---	08:30	
	09:30	ENGG MGT -L MR F105	WMCS -L BK F106	WMCS -L HK F107	OOP&DS -L SNJ G6	D2ECB1 D2ECB2 AC -P ISP/L -P GS PP EDC/L EDA/L	---	---	---	AOCS -P HSG FO&R/L	---	09:30	
	10:30	D4ECA1 D4ECA2 MC - MC - GPK CJ TI/L MP/L	DCS -L AKS F106	D3ECB1 D3ECB2 CN_DE_II ITC_DE_II -L -L KDS DS F107 COMP/L	LCS -L TDS G6	D2ECB1 D2ECB2 AC -P ISP/L -P GS PP EDC/L EDA/L	---	---	---		---	10:30	
	11:30	Mjr Proj AKS, NK, CJ, DS, SK, PP PROJ/L	-x-	-x-	AC -L GS G6	IM&DA -L SMM G7	---	---	---	-x-	---	11:30	
	12:30	-x-	D3ECA1 D3ECA2 CN_DE_II ITC_DE_II -L -L KDS DS MW/L COMP/L	D3ECB1 D3ECB2 MCES -T DCS -P NM GPK MP/L COMM/L	-x-	-x-	---	---	---	PROJ_M1 -P MR, BK FO&R/L	---	12:30	
	13:30	D4ECA1 D4ECA2 WSN_DE_IV MC_DE_IV -LCT -LCT HK PP F105 TI/L	IS/RC - ...	D3ECB1 D3ECB2 MCES -P DCS -P NM GPK MP/L COMM/L	EMFT -L CJ G6	D2ECB1 D2ECB2 ISP/L -P OOP&DS -P HLK GS EDA/L COMP/L	---	---	---		---	13:30	
	14:30	IS/RC - --	D3ECA1 D3ECA2 DCS -P MWR -P HK SK, PP COMM/L MW/L	D3ECB1 D3ECB2 MCES -P DCS -T NM GPK MP/L TI/L	IS/RC - ----	D2ECB1 D2ECB2 ISP/L -P OOP&DS -P HLK GS EDA/L COMP/L	D1EC 1/1 D1EC 1/2 MC - MC - NK SMM	D1EC 2/1 D1EC 2/2 MC - MC - KDS SNJ	---	Audit Course-2 - AMK	---	14:30	
15:30	IS/RC - --	D3ECA1 D3ECA2 DCS -P MWR -P HK SK, PP COMM/L MW/L	IS/RC - -----	IS/RC -		---	---	---	---	---	15:30		
Timetable generated with FET 5.34.3 on 1/17/20 10:56 AM													

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

		D4ECA	D3ECA	D3ECB	D2ECA	D2ECB	D1EC1	D1EC2	D1EC3	M1 EC	M2 EC		
W e d n e s d a y	08:30	OC -L NS F105	MWR -L PP F106	D3ECB1 D3ECB2 CN_DE_II ITC_DE_II -LCT -LCT KDS DS COMP/L F107	EMFT -L CJ G6	IM&DA -L SMM G7	---	---	---	---	---	08:30	W e d n e s d a y
	09:30	D4ECA1 D4ECA2 SC_DE_III COA_DE_III -LCT -LCT GPK NM F105 MP/L	MCES -L GJK F106	MWR -L SK F107	D2ECA1 D2ECA2 ISP/L - AC -P P GS SMM GS EDA/L EDC/L	LCS -L TDS G7	---	PPS LAB - RK COMP/L	---	AME_PE_4 -L MR G8	---	09:30	
	10:30	Mjr Proj NS, GJK, KDS, NM, HSG, HLK PROJ/L	DCS -L AKS F106	WMCS -L HK F107	D2ECA1 D2ECA2 ISP/L - AC -P P GS SMM GS EDA/L EDC/L	D2ECB1 D2ECB2 AC -T LCS -T SNJ TDS COMM/ L TI/L	---		---	AOCS -L BK G8	---	10:30	
	11:30	VLSID -L GJK F105	-x-	-x-	OOP&DS -L SNJ G6	OOP&DS -L HLK G7	---	---	---	-x-	---	11:30	
	12:30	-x-	IS/RC - --	MCES -L NM G6	-x-	-x-	---	---	---	---	---	12:30	
	13:30	D4ECA1 D4ECA2 WSN_DE_IV MC_DE_IV -L -L HK PP F105 TI/L	D3ECA1 D3ECA2 CN_DE_II ITC_DE_II -L -L KDS DS EDC/L COMP/L	D3ECB1 D3ECB2 DCS -P MWR -P GPK CJ COMM/L MW/L	IM&DA -L SMM G6	AC -L SNJ G7	---	---	---	ADSP -P NK EDA/L	Thesis MR, HSG FO&R/L	13:30	
	14:30	D4ECA1 D4ECA2 ENGG MGT -T OC -T MR NS F105 TI/L	D3ECA1 D3ECA2 DCS -T MWR -T AKS SK G8 MP/L	D3ECB1 D3ECB2 DCS -P MWR -P GPK CJ COMM/L MW/L	D2ECA1 D2ECA2 LCS -T AC -T TDS GS EDC/L PROJ/L	IS/RC -	---	---	---	---	---	14:30	
	15:30	IS/RC - ----	OE_1_D3 - OE_T1 OE_ROOM-1	OE_2_D3 - OE_T2 OE_ROOM_2	IS/RC - ----		---	---	---	---	---	15:30	
Timetable generated with FET 5.34.3 on 1/17/20 10:56 AM													

	D4ECA	D3ECA	D3ECB	D2ECA	D2ECB	D1EC1	D1EC2	D1EC3	M1 EC	M2 EC		
Th u r s d a y	08:30	OC -L NS F105	D3ECA1 D3ECA2 CN_DE_II ITC_DE_II -L -L KDS DS TI/L F106	DCS -L GPK F107	OOP&DS -L SNJ G6	LCS -L TDS G7	---	---	---	AOCS -L BK G8	---	08:30
	09:30	D4ECA1 D4ECA2 OC -P VLSID -P NS HLK COMM/L COMP/L	MWR -L PP F106	MCES -L NM F107	EMFT -L CJ G6	D2ECB1 D2ECB2 STRW/L M&C/L - -P P SNJ HSG EDA/L EDC/L	---	---	---	AME_PE_4 -L MR G8	---	09:30
	10:30	D4ECA1 D4ECA2 OC -P VLSID -P NS HLK COMM/L COMP/L	MCES -L GJK F106	Minor Proj_B MR PROJ/L	AC -L GS G6	D2ECB1 D2ECB2 STRW/L M&C/L - -P P SNJ HSG EDA/L EDC/L	---	---	---	ADSP -L NK G8	---	10:30
	11:30	D4ECA1 D4ECA2 SC_DE_III COA_DE_III -L -L GPK NM F105 TI/L	-x-	-x-	D2ECA1 D2ECA2 MC - MC - HK GJK EDA/L PROJ/L	D2ECB1 D2ECB2 MC - MC - PP GS MW/L G8	---	---	D1EC D1EC 3/1 3/2 MC - MC - TDS HSG EDC/L COMM/L	-x-	---	11:30
	12:30	-x-	Minor Proj_A NS PROJ/L	D3ECB1 D3ECB2 MWR -P WMCS -T CJ HK MW/L TI/L	-x-	-x-	---	---	---	ADSP -P NK EDA/L	---	12:30
	13:30	VLSID -L GJK F105	D3ECA1 D3ECA2 WMCS -T DCS -T BK AKS COMP/L TI/L	D3ECB1 D3ECB2 MWR -P MCES -P CJ NM MW/L MP/L	IS/RC -	IM&DA -L SMM G7	---	---	---	---	---	13:30
	14:30	ENGG MGT -L MR F105	IS/RC - -----	D3ECB1 D3ECB2 MWR -T MCES -P SK NM COMM/L MP/L	D2ECA1 D2ECA2 AC -P OOP&DS - P GS HLK EDC/L COMP/L	IS/RC - -----	---	---	---	Audit Course-2 - AMK	---	14:30
	15:30	IS/RC -		D3ECB1 D3ECB2 WMCS - MWR -T T HK SK G7 MP/L	D2ECA1 D2ECA2 AC -P OOP&DS - P GS HLK EDC/L COMP/L		---	---	---	---	15:30	
Timetable generated with FET 5.34.3 on 1/17/20 10:56 AM												

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

	D4ECA	D3ECA	D3ECB	D2ECA	D2ECB	D1EC1	D1EC2	D1EC3	M1 EC	M2 EC		
F r i d a y	08:30	VLSID -L GJK F105	MWR -L SK F106	WMCS -L HK F107	LCS -L TDS G6	OOP&DS -L HLK G7	---	---	PPS LAB - AG COMP/L	ADSP -L NK G8	---	08:30
	09:30	ENGG MGT -L MR F105	WMCS -L BK F106	DCS -L GPK F107	IM&DA -L SMM G6	EMFT -L CJ G7	---	---		AWMC_PE_3 -L HSG G8	---	09:30
	10:30	D4ECA1 D4ECA2 VLSID -T ENGG MGT -T GJK MR F105 G6	DCS -L AKS F106	MWR -L SK F107	D2ECA1 D2ECA2 OOP&DS - STRW/L - P P GS TDS COMP/L EDA/L	D2ECB1 D2ECB2 M&C/L -P STRW/L - P P HSG SNJ EDC/L MP/L	---	---	---	---	---	10:30
	11:30	D4ECA1 D4ECA2 WSN_DE_IV MC_DE_IV -L -L HK PP F105 G7	-x-	-x-	D2ECA1 D2ECA2 OOP&DS - STRW/L - P P GS TDS COMP/L EDA/L	D2ECB1 D2ECB2 M&C/L -P STRW/L - P P HSG SNJ EDC/L MP/L	---	---	---	-x-	Thesis DS FO&R/L	11:30
	12:30	-x-	D3ECA1 D3ECA2 MCES -P DCS -P GJK, HK SMM MP/L COMM/L	D3ECB1 D3ECB2 CN_DE_II - ITC_DE_II - L L KDS DS G7 COMP/L	-x-	-x-	---	---	---	AOCS -L BK G8	Thesis AKS, NK FO&R/L	12:30
	13:30	IS/RC - --	D3ECA1 D3ECA2 MCES -P DCS -P GJK, HK SMM MP/L COMM/L	D3ECB1 D3ECB2 DCS -T MCES -T GPK NM PROJ/L MW/L	D2ECA1 D2ECA2 STRW/L - M&C/L -P P P NK KDS COMP/L EDC/L	LCS -L TDS G7	---	---	---	PROJ_M1 -P MR, BK FO&R/L	---	13:30
	14:30	IS/RC - ..	D3ECA1 D3ECA2 MWR -T MCES -T SK GJK MP/L TI/L	IS/RC - --	D2ECA1 D2ECA2 STRW/L - M&C/L -P P P NK KDS COMP/L EDC/L	AC -L SNJ G7	---	---	---		---	14:30
15:30	Mjr Proj NS, HK, GPK, TDS, SMM, GS PROJ/L	OE_1_D3 - OE_T1 OE_ROOM-1	OE_2_D3 - OE_T2 OE_ROOM_2	IS/RC - --	IS/RC -	---	---	---	---	---	15:30	
Timetable generated with FET 5.34.3 on 1/17/20 10:56 AM												