

**GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA**  
**(Department of Electronics & Communication Engineering)**  
**(An Autonomous College under UGC Act.)**

No.ECE/

Date:

**NOTICE**

Date sheet for 3<sup>rd</sup> Internal Test is as follows:

Date	Time	B.Tech (ECE)			M.Tech (ECE)	
		3 <sup>rd</sup> Sem.	5 <sup>th</sup> Sem.	7 <sup>th</sup> Sem.	1 <sup>st</sup> Sem.	3 <sup>rd</sup> Sem.
21.11.2018	10:30am to 12:00pm	NAS	LIC	OC	AES / SDTM	MMC / LPVD
21.11.2018	2:30pm to 4:00pm	EVS	TQM	VLSID	Ad MATH	--
22.11.2018	8:30am to 10:00am	MATHS-III	DSP	E. MGT	Ad OCS	--
22.11.2018	10:30am to 12:00pm	EMI	ACS	WSN / M.COMP	SCOMP / SIP	*O.E
26.11.2018	10:30am to 12:00pm	EDC-I	MPI	SC / COA	Ad CS	--
26.11.2018	2:30pm to 4:00pm	OOP&DS	AWP	--	--	--

**\* Note: M.Tech students consult your subject teacher for the syllabus, exam timing and seating plan.**

HOD (ECE)