

**GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA**  
**(Department of Electronics & Communication Engineering)**  
**(An Autonomous College under UGC Act.)**

No.ECE/

Date:

**NOTICE**

Date sheet for 2<sup>nd</sup> Internal Test is as follows. The detailed seating plan will be displayed on the department notice board on 12.11.2018.

| Date       | Time               | B.Tech (ECE)         |                      |                      | M.Tech (ECE)         |                      |
|------------|--------------------|----------------------|----------------------|----------------------|----------------------|----------------------|
|            |                    | 3 <sup>rd</sup> Sem. | 5 <sup>th</sup> Sem. | 7 <sup>th</sup> Sem. | 1 <sup>st</sup> Sem. | 3 <sup>rd</sup> Sem. |
| 12.11.2018 | 10:30am to 12:00pm | NAS                  | LIC                  | OC                   | AES / SDTM           | MMC / LPVD           |
| 12.11.2018 | 2:30pm to 4:00pm   | EVS                  | TQM                  | --                   | --                   | --                   |
| 13.11.2018 | 10:30am to 12:00pm | MATHS-III            | DSP                  | VLSID                | Ad MATH              | *O.E                 |
| 14.11.2018 | 10:30am to 12:00pm | EMI                  | ACS                  | E. MGT               | Ad OCS               | --                   |
| 19.11.2018 | 10:30am to 12:00pm | EDC-I                | MPI                  | WSN / M.COMP         | SCOMP / SIP          | --                   |
| 20.11.2018 | 10:30am to 12:00pm | OOP&DS               | AWP                  | SC / COA             | Ad CS                | --                   |

**\* Note: M.Tech students consult your subject teacher for the syllabus, exam timing and seating plan.**

**Syllabus for 2<sup>nd</sup> internal test for each subject is as under:-**

**B. TECH(ECE)**

|           |  |
|-----------|--|
| NAS       | Chapter – 3, 4, 5 of syllabus.   |
| EVS       | Part-B of syllabus.  |
| MATHS-III | Unit 2, 4 & 5 of syllabus.   |
| EMI       | Sec – A Unit 3, 4, 5, 6 (syllabus covered after MST-I).<br>Sec-B Unit 4, 5, 6.                     |
| EDC-I     | Unit 4, 5 of syllabus.   |
| OOP&DS    | Unit 3, 5, 6 & 7 of syllabus.  |
| LIC       | Unit 3, 4 of syllabus.   |
| TQM       | Unit 3, 4, 5 of syllabus.  |
| DSP       | Unit 4, 5, 6, 7 of syllabus.   |
| ACS       | Sec – B Chapter 3, 4 (remaining) & 5.  |
| MPI       | Sec – B Unit 3, 6 complete, Unit 4 IC chips (8279, 8237, 8259), Unit 5 1 <sup>st</sup> two topics. |
| AWP       | Unit 4, 5, 6 of syllabus.  |
| OC        | Unit 4 and Unit 5.   |
| VLSID     | Unit 2, 5, 6 of syllabus.  |
| E. MGT    | Unit 2, 5, 6, 8 of syllabus.   |

|        |                            |
|--------|----------------------------|
| WSN    | Unit 3, 4 & 5 of syllabus. |
| M.COMP | Unit 3, 4 of syllabus.     |
| SC     | Unit 3, 4 & 6 of syllabus. |
| COA    | Unit 3, 4 of syllabus.     |

**M.TECH(ECE)**

|         |                                       |
|---------|---------------------------------------|
| AES     | <b>Unit 4, 5, 6 of syllabus.</b>      |
| SDTM    | <b>Chapter 3, 4, 5 of syllabus.</b>   |
| Ad MATH | <b>Unit 2, 3, 4 of syllabus.</b>      |
| Ad OCS  | <b>Unit 4 &amp; 5 of syllabus.</b>    |
| SCOMP   | <b>Unit 3 – 6 of syllabus.</b>        |
| SIP     | <b>Unit 4, 5, 6 of syllabus.</b>      |
| Ad CS   | <b>Unit 2, 4, 5 of syllabus.</b>      |
| MMC     | <b>Half Unit 2, 4, 5 of syllabus.</b> |
| LPVD    | <b>Unit 4, 5, 6 of syllabus.</b>      |

HOD (ECE)

CC to:

1. DNB
2. Circulation to all concerned faculty members.
3. Office copy.
4. Prof. Jasmeet Kaur.