

GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA
(Department of Electronics & Communication Engineering)
(An Autonomous College under UGC Act.)

NOTICE

In continuation of our letter no. ECE/274 dated. 24.08.2017. The following student detained in previous classes has been allowed to attend classes for completing their attendance as per the schedule given below:-

Sr. No.	Name of Student & Roll No.	Subject	Schedule	Name of Teacher assigned
1	Akshpreet Kaur (1311255)	Lab OOP using C++ & DS (EC-14308)	Along with class of D2EC A2 (P) (Mon-6 th ,7 th)	Pf. Chahat Jain
			Along with class of D2EC B2 (P) (Tues-6 th ,7 th)	Pf. Harminder Kaur Aulakh
			Along with class of D2EC A1 (P) (fri-6 th ,7 th)	Pf. Chahat Jain
		Lab DCLD (BTEC-309)	(Mon-5 th ,6 th)	Dr. Balwinder Singh Dhaliwal
			(Tues-5 th ,6 th)	
			(Wed-5 th ,6 th)	

HOD (ECE)