

Grants

Session (July, 2013 - June, 2014)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1	Dr. Sandeep Singh Gill	National Conference RAECT-13	TEQIP, SERB and PTU (National Agency)	Grants	225,000	2 days	2013	Completed
2	Dr. Munish Rattan							

Session (July, 2014 - June, 2015)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1	Dr. Sandeep Singh Gill	FDP on Advanced VLSI Design Techniques	AICTE (National Agency)	Grants	700,000	1 week	Jan, 2014	Completed

Session (July, 2016 - June, 2017)

S No.	Name of Faculty	Name of Project	Funding Agency	Category	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1	Dr. Sandeep Singh Gill,	FDP (Technology and Management for Electronic Product Design)	TEQIP-II	Grants	5,00,000	Two weeks	May 2-13, 2016	Completed
2	Dr. Munish Rattan							
3	Dr. Narwant Singh							

4	Dr. Sandeep Singh Gill	International Conference (SCAWC-17)	TEQIP-II, TCC, GNDEC, CSIR, DRDO, Connect Broadband	Grants	3,4,0000	2 days	March, 2017	Completed
5	Dr. Sandeep Singh Gill	Workshop (Effective Teaching Pedagogy and Research Methodology)	TEQIP-II	Grants	1,00,000	2 days	March, 2017	Completed
6	Dr. Munish Rattan							
7	Dr. Narwant Singh							
8	Gurpurneet Kaur							
9	Naazia Makkar							
10	Dr. Sandeep Singh Gill	Design Modelling and optimization of Carbon Nanotube(CNT) bossed diaphragm based piezoresistive Nano-sensor (Serial Number-9495)	DST-WOS A	Grants	2,000,000	----	2017	Submitted

Session (July, 2017 - June, 2018)

S No.	Name of Faculty	Name of Project	Category	Funding Agency	Amount (Rupees)	Duration	Date of Starting & Completion	Remarks
1	Dr. Sandeep Singh Gill	MODROBS	AICTE	Grants	20,00,000	Two year	December, 2017	ongoing
2	Dr. Sandeep Singh Gill	National Seminar (Design Issues in Low Power VLSI Domain)	AICTE	Grants	1,00,000	One day	08-Nov-17	Complete
3	Dr. Sandeep Singh Gill	FDP (Design Challenges in Low Power VLSI Design)	AICTE	Grants	7,00,000	Two weeks	December 04-16, 2017	Complete

4	Dr. Sandeep Singh Gill	STTP(Design issues in Low power VLSI)	QIP	Grants	5,00,000	One week	March 12-16, 2018	Complete
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