GINDEC	JNDEC LUDHIANA							
			G6					
	Monday	Tuesday	Wednesday	Thursday	Friday			
	D2ECA	D2ECA		D2ECA	D2ECA			
08:30	KPS	SMM		NK	CJ			
	CA L	EVS L		SS L	NAS L			
	D2ECA	D2ECA	D2ECA	D2ECA	D2ECA			
09:30	NSG	NK	SS(AP)	KPS	NSG			
	DE L	SS L	EM-III L	CA L	DE L			
	D2ECA	D2ECA	D2ECA	D2ECA	D2ECA			
10:30	HKA	KPS	CJ	NSG	HKA			
	ED L	CA L	NAS L	DE L	ED L			
	D2ECA	D2ECA	D2ECA	D2ECA	D2ECA			
11:30	CJ	SS(AP)	NK	HKA	SMM			
	NAS L	EM-III L	SS L	ED L	EVS L			
12:30								
	D2ECA		D2564 D256D	D2564 D256D	D3ECA2			
1:30	SS(AP)		D3ECA, D3ECB	·	DS			
	EM-III L		NK(MBA) OB L	SJK(AP) COLL	DSP T			
2:30			Obl	COLL				
	D4WC	DAFCA DAFCE	DANAC	Dacces	DAMC			
3:30	HK	D4ECA, D4ECB	D4WC	D3ECB2 BK	D4WC HK			
3:50	WC(OE-II)	HK, GJK MC P	HK	MC P				
	L	IVIC P	WC(OE-II) L	IVIC P	WC(OE-II) L			
	Time	table generated	with FET 6.1.6	on 11/16/21 9:3	35 AM			
	J , .,							

GINDEC LUDHIANA							
			G7				
	Monday	Tuesday	Wednesday	Thursday	Friday		
	D2ECB	D2ECB	D2ECB		D2ECB		
08:30	SMM	NSG	KPS		НКА		
	EVS L	DE L	CA L		ED L		
	D2ECB	D2ECB	D2ECB	D2ECB	D2ECB		
09:30	CJ	SS(AP)	NK	HKA	KPS		
	NAS L	EM-III L	SS L	ED L	CA L		
	D2ECB	D2ECB	D2ECB	D2ECB	D2ECB		
10:30	NK	HKA	NSG	CJ	NK		
	SS L	ED L	DE L	NAS L	SS L		
	D2ECB	D2ECB	D2ECB	D2ECB	D2ECB		
11:30	NSG	CJ	SS(AP)	SMM	SS(AP)		
	DE L	NAS L	EM-III L	EVS L	EM-III L		
12:30							
		D2ECB					
1:30		KPS					
		CA L					
2:30							
		D2ECA1	D2ECB1				
3:30		GPK	NSG				
		MC P	DE T				
	Timetable	generated	with FET 6.1.6	on 11/16/2	1 9:35 AM		
	<u> </u>						

	Co							
			G8					
	Monday	Tuesday	Wednesday	Thursday	Friday			
	M1EC	N4456		N4456				
	ВК	M1EC		M1EC				
08:30	CCNC(E-2)	HK		HSG				
	L L	RM L		SoftCom (E-1) L				
		M1EC		M1EC				
09:30		HSG		BK				
		SoftCom (E-1) L		CCNC(E-2) L				
		M1EC	M1EC					
10:30		MR	GPK					
		MCS L	VDA L		<u> </u>			
	M1EC	M1EC						
11:30	HK	GPK						
	RM L	VDA L						
12:30								
		D3ECB1	M1EC		M1EC			
1:30		DS	BK		HK			
		DSP T	CCNC(E-2) L		RM L			
	M1EC		NATEC	N41FC	NATEC			
2.20	HSG		M1EC	M1EC	M1EC			
2:30	SoftCom		MR	GPK	MR			
	(E-1) L		MCS L	VDA L	MCS L			
		D2ECB2	D3ECA1					
3:30		MR	DS					
		MC P	DSP T					
	Timet	able generated v	with FET 6.1.6	on 11/16/21 9:3	5 AM			
	Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM							

GINDEC LUDHIANA							
			F-105				
	Monday	Tuesday	Wednesday	Thursday	Friday		
	D3ECA		D3ECA	D3ECA	D3ECA		
08:30	KDS		DS	GJK	SK		
	CN L		DSP L	MPMC L	AWP L		
	D3ECA	D3ECA	D3ECA	D3ECA	D3ECA		
09:30	SK	DS	GJK	SK	GJK		
	AWP L	DSP L	MPMC L	AWP L	MPMC L		
	D3ECA	D3ECA	D3ECA	D3ECA	D3ECA		
10:30	DS	AKS	KDS	AKS	KDS		
	DSP L	ACS L	CN L	ACS L	CN L		
11:30							
			D3ECA				
12:30			AKS				
			ACS L				
					D3ECA1		
1:30					GJK		
					MPMCT		
2:30							
				D3ECB1			
3:30				KPS			
				MC P			
	Timetable	generated	with FET 6.1.6	on 11/16/2	1 9:35 AM		
	Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM						

	0.1220 202						
		F-106					
	Monday	Tuesday	Wednesday	Thursday	Friday		
	D3ECB	D3ECB	D3ECB	D3ECB	D3ECB		
08:30	DS	GJK	GPK	MR	KDS		
	DSP L	MPMC L	ACS L	AWP L	CN L		
	D3ECB	D3ECB	D3ECB	D3ECB	D3ECB		
09:30	GJK	GPK	MR	KDS	DS		
	MPMC L	ACS L	AWP L	CN L	DSP L		
	D3ECB	D3ECB	D3ECB	D3ECB	D3ECB		
10:30	MR	KDS	DS	GJK	GPK		
	AWP L	CN L	DSP L	MPMC L	ACS L		
11:30	-X-	-X-	-X-	-X-	-X-		
12:30	-X-	-X-	-X-	-X-	-X-		
1:30	-X-	-X-	-X-	-X-	-X-		
2:30	-X-	-X-	-X-	-X-	-X-		
3:30	-X-	-X-	-X-	-X-	-X-		
	Timetable	generated	with FET 6.1.6	on 11/16/2	1 9:35 AM		

			MW/L		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30					
09:30					
10:30			D4ECA2 KPS VLSI(E-IV) L		
11:30	D4ECA2 KPS VLSI(E-IV) L				D4ECA2 KPS VLSI(E-IV) L
12:30					
1:30	D4ECA1 SMM ON (E-III) T	D2ECA2 BK, SMM DE-Lab P	D2ECB1 HK		D2ECA2 NSG DE T
2:30	D2ECA1	DE-Lab P	DE-Lab P	D2ECB2	
3:30	BK, SMM DE-Lab P	D2ECA3 HKA MC P		HK DE-Lab P	
	Timetable	generated	with FET 6.1.6	on 11/16/2	1 9:35 AM

GIVEL	EC LODRIANA							
			PROJ/L					
	Monday	Tuesday	Wednesday	Thursday	Friday			
		D4ECA		D4ECA				
08:30		GPK, CJ, HKA		BK, HK, SK, SMM				
		M.Proj P		M.Proj P				
09:30								
10:30								
11:30								
				D3ECB2				
12:30				DS				
				DSP T				
			D2ECA2	D4ECA	D2ECA1			
1:30			NK	GJK, DS, SMM	NK			
			SS T	M.Proj P	SS T			
	D4ECA	D4ECA		D2ECB1	D4ECA			
2:30	MR, NK,	NSG, HK, HSG		NK	BK, HK, SK			
2.50	KDS	M.Proj P		SS T	M.Proj P			
	M.Proj P	171.1 103 1		33 1	101.1 101 1			
		D1EC4	D2ECB2		D2ECB2			
3:30		SMM	NK		NSG			
		MC P	SS T		DE T			
	Timeta	able generated	with FET 6.1	.6 on 11/16/21 9:3	35 AM			

JINDEC LODRIANA						
			MP/L			
	Monday	Tuesday	Wednesday	Thursday	Friday	
08:30						
09:30						
			D4ECA1			
10:30			BK			
			MC(E-IV) L			
	D4ECA1				D4ECA1	
11:30	BK				BK	
	MC(E-IV) L				MC(E-IV) L	
12:30	D3ECA1				D3ECB2	
	GJK	D3ECB2			C	
1:30	MPMC-Lab	GJK			MPMC-Lab P	
	Р	MPMC T			IVIFIVIC-Lab F	
			D4ECA1			
2:30	D3ECB1		BK	DAECAS	Darcas	
	CJ		MC(E-IV) T	D4ECA2 MR	D3ECA2 GJK	
	MPMC-Lab	D1EC2		SRTECE P	MPMC-Lab P	
3:30	Р	SK		SKIECE	IVIF IVIC-Lab P	
		MC P				
	Timetable	generated	with FET 6.1.	6 on 11/16/	21 9:35 AM	

	LUDHIANA	-	COMM/L		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30					
09:30					
10:30					
11:30	M2EC AKS MMC(E-5) L				M2EC AKS MMC(E-5) L
12:30	D3ECB2 GPK			D3ECB1 GJK MPMC T	D3ECB1 GPK
1:30	ACS-Lab P				ACS-Lab P
2:30	D3ECA2		M2EC AKS MMC(E-5) L		D3ECA1
3:30	AKS ACS-Lab P	D1EC3 HSG MC P	D3ECA2 GJK MPMC T	D3ECB3 HSG MC P	AKS, KPS ACS-Lab P
	Timetable	generated	with FET 6.1.6	on 11/16/2	21 9:35 AM

UNDEC LODITIANA								
			FO&R/L					
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30			D4ECA1		M1EC			
09:30	M1EC	M2EC	HK		GPK			
09.30	GPK	BK	ON-Lab(E-III) P		VDA-Lab P			
10:30	VDA-Lab P	Pre-Thesis P		M1EC	M1EC			
11:30			M2EC	MR, HSG	MR, HSG			
11.50			MR	MCS-Lab P	MCS-Lab P			
12:30			Pre-Thesis P					
1:30								
2:30								
				D3ECA3				
3:30				KDS				
				MC P				
	Timetab	le generated	with FET 6.1.6 c	n 11/16/21	9:35 AM			

GINDLO	JINDEC LUDHIANA								
	EDA/L								
	Monday	Tuesday	Wednesday	Thursday	Friday				
08:30			-X-						
09:30			-X-						
	D4ECA2								
10:30	HSG								
	MOSFET(E-								
	III) L								
			D4ECA2						
11:30			HSG						
			Mosfet(e-III) L						
12:30	D3ECB1	D3ECA2							
	NK	SK			D4ECA2				
1:30	DSP-Lab P	DSP-Lab P			HSG				
	D3F-Lab F	D3F-Lab F			MOSFET(E-III) L				
			D4ECA2						
2:30	DOECA1		KPS		Darcha				
	D3ECA1		VLSI(E-IV) T		D3ECB2				
	SK DSP-Lab P	D2ECB1		D3ECA1	NK DSP-Lab P				
3:30	D2L-Fab L	CJ		NK	DSP-Lab P				
		MC P		MC P					
	Timeta	ble generat	ed with FET 6.1.6	on 11/16/2	21 9:35 AM				
		Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM							

		EDC/L					
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30							
09:30							
10:30	D4ECA1 SMM ON (E-III) L						
11:30			D4ECA1 SMM ON (E-III) L				
12:30							
1:30	D4ECA2 HSG MOSFET(E- III) T	D2ECA1 HKA ED-Lab P	D2ECB2 HKA ED-Lab P	D2ECA1 NSG DE T	D4ECA1 SMM ON (E-III) L		
2:30	D2ECA2			D4ECA1	D2ECB1		
3:30	NSG, HKA ED-Lab P	D2ECA2 NSG MC P		NSG SRTECE P	HKA ED-Lab P		
	Timetable	generated	with FET 6.1.6	on 11/16/2	1 9:35 AM		

	COMP/L							
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30			D4ECA2					
09:30			HSG MOSFET-Lab(E-III) P					
10:30								
11:30								
12:30	D3ECA2	D3ECA1						
1:30	KDS CN-Lab P	KDS CN-Lab P						
2:30	D3ECB2 DS CN-Lab P				D2FCB1			
3:30		D1EC1 DS MC P		D3ECA2 SMM MC P	D3ECB1 KDS CN-Lab P			
	Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM							

	TI/L							
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30								
09:30								
10:30								
11:30								
12:30								
			D2ECA1	D2ECA2				
1:30			CJ	CJ				
			NAS T	NAS T				
					D2ECB2			
2:30					CJ			
					NAST			
		D1EC5		D2ECB1				
3:30		KPS		CJ				
		MC P		NAS T				
	Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM							