

GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA
(Department of Electronics & Communication Engineering)

No.ECE/ 2357

Date: 19.02.2021

NOTICE

Date sheet of MSE-II for UG is as follows:

Date	Timing	3 rd Sem.
22.2.2021	11:00am to 12:30pm	Signals and Systems
23.2.2021	11:00am to 12:30pm	Network Analysis and Synthesis
24.2.2021	11:00am to 12:30pm	Electronic Devices
25.2.2021	9:30am to 11:00am	Engineering Mathematics – III
25.2.2021	2:30pm to 4:00pm	Computer Architecture
26.2.2021	9:30am to 11:00am	Digital Electronics
26.2.2021	2:30pm to 4:00pm	Environmental Science

Date sheet of MSE-II for PG is as follows:

Date	Timing	1 st Sem.
22.2.2021	11:00am to 12:30pm	SDTM
23.2.2021	9:30am to 11:00am	Audit Course
24.2.2021	11:00am to 12:30pm	RM & IPR
25.2.2021	11:00am to 12:30pm	VDA
26.2.2021	9:30am to 11:00am	MCS
26.2.2021	2:30pm to 4:00pm	CCNS

Venue: G-8

Syllabus:

Signals and Systems- Unit-3, 4 and 5(sampling theorem & types of noise)
Network Analysis and Synthesis- Unit-1, 3 and 4
Electronic Devices- Unit-3, 4 and 5
Engineering Mathematics III- Complex numbers, Partial Differential equations
Computer Architecture- Unit-3, 4 and 5
Environmental Science- Unit-4,5 and 6
Digital Electronics- Unit-2(K-Maps), Unit -3,4
SDTM- Unit-2(diode equation onwards), Unit-3,4.
VDA- Unit-3 and 4
RM&IPR- Unit-2, 5 and 6
MCS- Unit- 1& 3.
CCNS- Unit-1
Audit Course- Consult your subject teacher for syllabus and venue.

Anneeta Selma
HOD (ECE) 19/2/2021

CC to:

1. DNB
2. Circulation to all concerned faculty members.
3. Office copy.
4. Ms. Rajbir through their respective HOD (APP. Sci.)
5. Pf. Harminder Kaur to upload on website.