

ECE Not 2012
DATE 18.09.2020

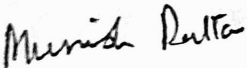
Guru Nanak Dev Engineering College, Ludhiana
Department of Electronics and Communication Engineering

Time-Table for M.Tech. (Part-Time)
Revised w.e.f. 19-09-2020

Day	Time	5th Semester
Saturday	9:00 am - 11:00 am	Pre-Thesis Project (HSG)
	11:00 am - 12:30 pm	VLSI Design Automation (GPK)
	2:30 pm - 4:00 pm	Open Elective: Disaster Reduction and Management (CE)
Sunday	9:30 am - 11:00 am	Open Elective: Disaster Reduction and Management (CE)
	11:00 am - 12:30 pm	VLSI Design Automation (GPK)
	2:00 pm - 3:00 pm	Pre-Thesis Project (HSG)
	3:00 pm - 4:00 pm	Pre-Thesis Seminar (NM)

GPK: Prof. Gurpurneet Kaur; NM: Prof. Naazia Makkar;
HSG: Dr. Harsimranjit Singh Gill; CE: Faculty from Civil Deptt.

Online Room: F-105 for ECE


Programme Coordinator
M.Tech. ECE