GNDEC LUDHIANA

		AKS	NSG	MR	BK	HK	GJK	NK	GPK	CJ	KDS	HKA	DS	SK	SMM	HSG	KPS
Monday	**08:30**	-x-	---	---	M1EC CCNC(E-2) L G8	---	---	---	---	---	D3ECA CN L F-105	---	D3ECB DSP L F-106	---	D2ECB EVS L G7	---	D2ECA CA L G6
	09:30	-x-	D2ECA DE L G6	---	---	---	D3ECB MPMC L F-106	---	M1EC VDA-Lab P FO&R/L	D2ECB NAS L G7	---	---	---	D3ECA AWP L F-105	---	---	---
	10:30	---	---	D3ECB AWP L F-106	---	---	---	D2ECB SS L G7		---	---	D2ECA ED L G6	D3ECA DSP L F-105	---	D4ECA1 ON (E-III) L EDC/L	D4ECA2 MOSFET(E-III) L EDA/L	---
	11:30	M2ECM MC(E-5) L COMM/L	D2ECB DE L G7	---	D4ECA1 MC(E-IV) L MP/L	M1EC RM L G8	---	---	---	D2ECA NAS L G6	---	---	---	---	---	---	D4ECA2 VLSI(E-IV) L MW/L
	12:30	---	-x-	---	-x-	---				---		---	---	---	---	---	---
	1:30	-x-	---	-x-	---	---	D3ECA1 MPMC-Lab P MP/L	D3ECB1 DSP-Lab P EDA/L	D3ECB2 ACS-Lab P COMM/L	---	D3ECA2 CN-Lab P COMP/L	---	---	D2CEB Basic_Ec L G-3	D4ECA1 ON (E-III) T MW/L	D4ECA2 MOSFET(E-III) T EDC/L	
	2:30	D3ECA2 ACS-Lab P COMM/L	MR, NK, KDS D4ECA M.Proj P PROJ/L	BK, SMM	---	---	MR, NK, KDS D4ECA M.Proj P PROJ/L	---	MR, NK, KDS D4ECA M.Proj P PROJ/L		NSG, HKA			M1EC SoftCom (E-1) L G8	IT-A2 Phy Lab P Phy Lab
	3:30		NSG, HKA D2ECA2 ED-Lab P EDC/L	D2ECA1 DE-Lab P MW/L	D4WC WC(OE-II) L G6	---	---	---	---	D3ECB1 MPMC-Lab P MP/L	D2ECA2 ED-Lab P EDC/L	D3ECB2 CN-Lab P COMP/L	D3ECA1 DSP-Lab P EDA/L	BK, SMM D2ECA1 DE-Lab P MW/L	---	---

Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM

GNDEC LUDHIANA

		NSG	MR	BK	HK	GJK	NK	GPK	CJ	KDS	HKA	DS	SK	SMM	HSG	KPS
T u e s d a y	**08:30** -x-	D2ECB DE L G7	---	---	M1EC RM L G8	D3ECB MPMC L F-106	---	GPK, CJ, HKA D4ECA M.Proj P PROJ/L	GPK, CJ, HKA D4ECA M.Proj P PROJ/L	---	GPK, CJ, HKA D4ECA M.Proj P PROJ/L	---	---	D2ECA EVS L G6	-x-	---
	09:30 -x-	---	---		---	---	D2ECA SS L G6	D3ECB ACS L F-106	---	---	---	D3ECA DSP L F-105	D2CEA Basic_Ec L F-103	---	M1EC SoftCom (E-1) L G8	---
	10:30 D3ECA ACS L F-105	---	M1EC MCS L G8	M2EC Pre-Thesis P FO&R/L	---	---	---	---	---	D3ECB CN L F-106	D2ECB ED L G7	---	---	---	---	D2ECA CA L G6
	11:30 ---	---	---	---	---	---	---	M1EC VDA L G8	D2ECB NAS L G7	---	---	---	---	D2CEB Basic_Ec L F-103	---	---
	12:30 ---	-x-	---	-x-	---	---	---	---	---	D3ECA1 CN-Lab P COMP/L	---	---	D3ECA2 DSP-Lab P EDA/L	---	---	---
	1:30 -x-	---	-x-	BK, SMM D2ECA2 DE-Lab P MW/L	---	D3ECB2 MPMC T MP/L	---	---	---		D2ECA1 ED-Lab P EDC/L	D3ECB1 DSP T G8		BK, SMM D2ECA2 DE-Lab P MW/L	---	D2ECB CA L G7
	2:30 ---	NSG, HK, HSG D4ECA M.Proj P PROJ/L	---		NSG, HK, HSG D4ECA M.Proj P PROJ/L	---	---	---	---	D3ECA, D3ECB T&P		---	---		NSG, HK, HSG D4ECA M.Proj P PROJ/L	---
	3:30 ---	D2ECA2 MC P EDC/L	D2ECB2 MC P G8	---	HK, GJK D4ECA, D4ECB MC P G6	HK, GJK D4ECA, D4ECB MC P G6	---	D2ECA1 MC P G7	D2ECB1 MC P EDA/L		D2ECA3 MC P MW/L	D1EC1 MC P COMP/L	D1EC2 MC P MP/L	D1EC4 MC P PROJ/L	D1EC3 MC P COMM/L	D1EC5 MC P TI/L

Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM

GNDEC LUDHIANA

	AKS	NSG	MR	BK	HK	GJK	NK	GPK	CJ	KDS	HKA	DS	SK	SMM	HSG	KPS
08:30	-x-	---	---	---	D4ECA1 ON-Lab(E-III) P FO&R/L	---	---	D3ECB ACS L F-106	---	---	---	D3ECA DSP L F-105	---	---	D4ECA2 MOSFET-Lab(E-III) P COMP/L	D2ECB CA L G7
09:30	-x-	---	D3ECB AWP L F-106	---		D3ECA MPMC L F-105	D2ECB SS L G7	---	---	---	---	---	D2CEA Basic_Ec L F-104	---		---
10:30	---	D2ECB DE L G7	---	D4ECA1 MC(E-IV) L MP/L	---	---	---	M1EC VDA L G8	D2ECA NAS L G6	D3ECA CN L F-105	---	D3ECB DSP L F-106	---	---	---	D4ECA2 VLSI(E-IV) L MW/L
11:30	---	---	M2EC Pre-Thesis P FO&R/L	---	---	---	D2ECA SS L G6	---	---	---	---	---	---	D4ECA1 ON (E-III) L EDC/L	D4ECA2 MOSFET(E-III) L EDA/L	---
12:30	D3ECA ACS L F-105	-x-		-x-	---	---	---	---	---	---	---	---	---	---	---	---
1:30	-x-	---	-x-	M1EC CCNC(E-2) L G8		---	D2ECA2 SS T PROJ/L	---	D2ECA1 NAS T TI/L	---	---	---	---	---	---	---
2:30	M2EC MMC(E-5) L COMM/L	---	M1EC MCS L G8	D4ECA1 MC(E-IV) T MP/L	D2ECB1 DE-Lab P MW/L	---	---	---	---	---	---	D2ECB2 ED-Lab P EDC/L	---	---	---	D4ECA2 VLSI(E-IV) T EDA/L
3:30	---	D2ECB1 DE T G7	---	---	D4WC WC(OE-II) L G6	D3ECA2 MPMC T COMM/L	D2ECB2 SS T PROJ/L	---	---	---	---	D3ECA1 DSP T G8	---	---	---	---

Row label (leftmost): **Wednesday**

Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM

GNDEC LUDHIANA

		AKS	NSG	MR	BK	HK	GJK	NK	GPK	CJ	KDS	HKA	DS	SK	SMM	HSG	KPS
T h u r s d a y	**08:30**	-x-	---	D3ECB AWP L F-106	BK, HK, SK, SMM D4ECA M.Proj P PROJ/L	BK, HK, SK, SMM D4ECA M.Proj P PROJ/L	D3ECA MPMC L F-105	D2ECA SS L G6	---	---	---	---	---	BK, HK, SK, SMM D4ECA M.Proj P PROJ/L	BK, HK, SK, SMM D4ECA M.Proj P PROJ/L	M1EC SoftCom (E-1) L G8	---
	09:30	-x-	---	---	M1EC CCNC(E-2) L G8	---	---	---	---	---	D3ECB CN L F-106	D2ECB ED L G7	---	D3ECA AWP L F-105	---	---	D2ECA CA L G6
	10:30	D3ECA ACS L F-105	D2ECA DE L G6	MR, HSG M1EC MCS-Lab P FO&R/L	---	---	D3ECB MPMC L F-106	---	---	D2ECB NAS L G7	---	---	---	---	---	MR, HSG M1EC MCS-Lab P FO&R/L	---
	11:30	---	---	---	---	---	---	---	---	---	---	D2ECA ED L G6	---	D2CEB Basic_Ec L F-107	D2ECB EVS L G7	---	---
	12:30	---	-x-	---	-x-	---	D3ECB1 MPMC T COMM/L	---	---	---	---	---	D3ECB2 DSP T PROJ/L	---	---	---	---
	1:30	-x-	D2ECA1 DE T EDC/L	-x-	---	---	GJK, DS, SMM D4ECA M.Proj P PROJ/L	---	---	D2ECA2 NAS T TI/L	---	---	GJK, DS, SMM D4ECA M.Proj P PROJ/L	D2CEA Basic_Ec L F-107	GJK, DS, SMM D4ECA M.Proj P PROJ/L	---	---
	2:30	-x-	D4ECA1 SRTECE P EDC/L	D4ECA2 SRTECE P MP/L	---	D2ECB2 DE-Lab P MW/L	---	D2ECB1 SS T PROJ/L	M1EC VDA L G8	---	---	---	-x-	---	---	---	---
	3:30	-x-		D3ECB2 MC P G6			---	D3ECA1 MC P EDA/L	---	D2ECB1 NAS T TI/L	D3ECA3 MC P FO&R/L	---	-x-	---	D3ECA2 MC P COMP/L	D3ECB3 MC P COMM/L	D3ECB1 MC P F-105

Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM

GNDEC LUDHIANA

Friday	AKS	NSG	MR	BK	HK	GJK	NK	GPK	CJ	KDS	HKA	DS	SK	SMM	HSG	KPS
08:30	-x-	---	---	---	---	---	---	M1EC VDA-Lab P FO&R/L	D2ECA NAS L G6	D3ECB CN L F-106	D2ECB ED L G7	---	D3ECA AWP L F-105	---	---	---
09:30	-x-	D2ECA DE L G6	---	---	---	D3ECA MPMC L F-105	---		---	---	---	D3ECB DSP L F-106	---	---	---	D2ECB CA L G7
10:30	---	---		---	---	---	D2ECB SS L G7	D3ECB ACS L F-106	---	D3ECA CN L F-105	D2ECA ED L G6	---	---	---		---
11:30	M2EC MMC(E-5) L COMM/L	---	MR, HSG M1EC MCS-Lab P FO&R/L	D4ECA1 MC(E-IV) L MP/L	---	---	---	---	---	---	---	---	---	D2ECA EVS L G6	MR, HSG M1EC MCS-Lab P FO&R/L	D4ECA2 VLSI(E-IV) L MW/L
12:30	---	-x-	---	-x-	---	---	---			---	---	---	---	---	---	---
1:30	-x-	D2ECA2 DE T MW/L	-x-	---	M1EC RM L G8	D3ECA1 MPMC T F-105	D2ECA1 SS T PROJ/L	D3ECB1 ACS-Lab P COMM/L	D3ECB2 MPMC-Lab P MP/L	---	---	D3ECA2 DSP T G6	---	D4ECA1 ON (E-III) L EDC/L	D4ECA2 MOSFET(E-III) L EDA/L	---
2:30	AKS, KPS	---	M1EC MCS L G8	BK, HK, SK D4ECA M.Proj P PROJ/L	BK, HK, SK D4ECA M.Proj P PROJ/L	---	---	---	D2ECB2 NAS T TI/L	---	---	---	BK, HK, SK D4ECA M.Proj P PROJ/L	---	---	---
3:30	D3ECA1 ACS-Lab P COMM/L	D2ECB2 DE T PROJ/L	---	---	D4WC WC(OE-II) L G6	D3ECA2 MPMC-Lab P MP/L	D3ECB2 DSP-Lab P EDA/L	---	---	D3ECB1 CN-Lab P COMP/L	D2ECB1 ED-Lab P EDC/L	---	---	---	---	AKS, KPS D3ECA1 ACS-Lab P COMM/L

Timetable generated with FET 6.1.6 on 11/16/21 9:35 AM