

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f
06-01-2020

| | AKS | | | | | |
|---|-------------------------|--------------------------|--|---------------|--------------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | --- | --- | --- | 08:30 |
| 09:30 | --- | --- | --- | --- | --- | 09:30 |
| 10:30 | D3ECA DCS -L F106 | D3ECA DCS -L F106 | --- | --- | D3ECA DCS -L F106 | 10:30 |
| 11:30 | --- | --- | AKS, NK, CJ, DS, SK, PP D4ECA Mjr Proj F105 | --- | --- | 11:30 |
| 12:30 | --- | --- | --- | --- | Minor Proj -P | 12:30 |
| 13:30 | --- | --- | --- | Minor Proj -P | --- | 13:30 |
| 14:30 | Minor Proj -P | D3ECA1 DCS -T TI/L | --- | --- | D3ECA2 DCS -T G8 | 14:30 |
| 15:30 | --- | --- | --- | --- | AKS, NK M2 EC Thesis FO&R/L | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | NS | | | | | |
|---|---|-------------------------------------|-------------------------------|------------------------|---------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | D4ECA OC -L F105 | --- | D4ECA OC -L F105 | --- | --- | 08:30 |
| 09:30 | --- | NS, DS M2 EC Thesis FO&R/L | --- | --- | --- | 09:30 |
| 10:30 | --- | | D3ECA Minor Proj_A F106 | --- | | 10:30 |
| 11:30 | NS, GJK, KDS, NM, HSG, HLK D4ECA Mjr Proj F105 | D4ECA1 OC -P COMM/L | --- | D4ECA OC -L F105 | D4ECA2 OC -P COMM/L | 11:30 |
| 12:30 | --- | --- | --- | --- | --- | 12:30 |
| 13:30 | --- | D3ECA1 MC - G6 | --- | --- | --- | 13:30 |
| 14:30 | D4ECA1 OC -T F105 | --- | --- | --- | --- | 14:30 |
| 15:30 | NS, HK, GPK, TDS, SMM, GS D4ECA Mjr Proj F105 | --- | --- | --- | D4ECA2 OC -T COMM/L | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | MR | | | | | |
|---|---------------------------------|-------------------------------|----------------------------|--------------------------------------|------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | --- | --- | --- | 08:30 |
| 09:30 | --- | D4ECA ENGG MGT -L F105 | M1 EC AME_PE_4 -L G8 | M1 EC AME_PE_4 -L G8 | M1 EC AME_PE_4 -L G8 | 09:30 |
| 10:30 | D4ECA ENGG MGT -L F105 | --- | --- | D3ECB Minor Proj_B F107 | --- | 10:30 |
| 11:30 | --- | --- | --- | --- | --- | 11:30 |
| 12:30 | MR, BK M1 EC | --- | --- | --- | --- | 12:30 |
| 13:30 | PROJ_M1 - P FO&R/L | D3ECA2 MC - PROJ/L | Minor Proj -P | --- | D4ECA ENGG MGT -L F105 | 13:30 |
| 14:30 | D4ECA2 ENGG MGT -T G7 | D4ECA1 ENGG MGT -T F105 | MR, BK M1 EC | --- | --- | 14:30 |
| 15:30 | --- | --- | PROJ_M1 -P FO&R/L | MR, HSG M2 EC Thesis FO&R/L | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | BK | | | | | |
|---|-----------------------------------|---------------------------|----------------------------|-------------------------------------|------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | D3ECA WMCS -L F106 | --- | BK, HK M2 EC Thesis FO&R/L | --- | 08:30 |
| 09:30 | D3ECA WMCS -L F106 | --- | D3ECA WMCS -L F106 | --- | --- | 09:30 |
| 10:30 | --- | M1 EC AOCS -L G8 | --- | M1 EC AOCS -L G8 | M1 EC AOCS -L G8 | 10:30 |
| 11:30 | --- | --- | --- | --- | --- | 11:30 |
| 12:30 | MR, BK M1 EC PROJ_M1 - P | --- | D3ECA2 WMCS -T EDA/L | --- | --- | 12:30 |
| 13:30 | FO&R/L | --- | --- | --- | Minor Proj -P | 13:30 |
| 14:30 | --- | --- | MR, BK M1 EC | --- | Minor Proj -P | 14:30 |
| 15:30 | --- | D3ECA1 WMCS -T TI/L | PROJ_M1 -P FO&R/L | Minor Proj -P | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | | HK | | | | | |
|---|--|--------------------------------|--------------------------------|-------------------------------------|-----------------------------|--------------|--|
| | | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | D4ECA1 WSN_DE_IV -L F105 | --- | BK, HK M2 EC Thesis FO&R/L | D3ECB WMCS -L F107 | 08:30 | |
| 09:30 | D4ECA1 WSN_DE_IV -L F105 | --- | D4ECA1 WSN_DE_IV -L F105 | D3ECB WMCS -L F107 | --- | 09:30 | |
| 10:30 | --- | D3ECB WMCS -L F107 | --- | --- | D2ECA2 ISP/L -P EDA/L | 10:30 | |
| 11:30 | --- | --- | --- | --- | | 11:30 | |
| 12:30 | --- | --- | --- | D3ECB2 WMCS -T COMP/L | --- | 12:30 | |
| 13:30 | --- | --- | --- | D4ECA1 WSN_DE_IV -L F105 | --- | 13:30 | |
| 14:30 | --- | | D3ECB1 WMCS -T MP/L | --- | --- | 14:30 | |
| 15:30 | NS, HK, GPK, TDS, SMM, GS D4ECA Mjr Proj F105 | D2ECA1 ISP/L -P EDA/L | D2ECA1 MC - MW/L | --- | --- | 15:30 | |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | | GJK | | | | | |
|---|---|--------------------------|---------------------------|---------------------------|----------------------------|---------------|--|
| | | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | D3ECA MCES -L F106 | D4ECA VLSID -L F105 | --- | 08:30 | |
| 09:30 | --- | D3ECA MCES -L F106 | --- | --- | D4ECA VLSID -L F105 | 09:30 | |
| 10:30 | --- | --- | D4ECA VLSID -L F105 | D3ECA MCES -L F106 | --- | 10:30 | |
| 11:30 | NS, GJK, KDS, NM, HSG, HLK D4ECA Mjr Proj F105 | --- | --- | --- | --- | 11:30 | |
| 12:30 | --- | --- | D3ECA1 MCES -T MW/L | D3ECA2 MCES -P MP/L | --- | 12:30 | |
| 13:30 | --- | --- | --- | --- | GJK, SMM | 13:30 | |
| 14:30 | D3ECA2 MCES -T MW/L | D4ECA2 VLSID -T G7 | --- | --- | D3ECA1 MCES -P MP/L | 14:30 | |
| 15:30 | --- | --- | D2ECA2 MC - F105 | --- | D4ECA1 VLSID -T F105 | 15:30 | |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | NK | | | | | |
|---|-------------------------------|------------------------|--|---------------------------|--------------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | --- | M1 EC ADSP -L G8 | --- | 08:30 |
| 09:30 | M1 EC ADSP -L G8 | --- | --- | --- | --- | 09:30 |
| 10:30 | --- | --- | --- | --- | --- | 10:30 |
| 11:30 | D2ECA1 STRW/L -P COMM/L | --- | AKS, NK, CJ, DS, SK, PP D4ECA Mjr Proj F105 | --- | --- | 11:30 |
| 12:30 | --- | --- | M1 EC ADSP -L G8 | M1 EC ADSP -P EDA/L | --- | 12:30 |
| 13:30 | Minor Proj -P | --- | --- | Minor Proj -P | --- | 13:30 |
| 14:30 | M1 EC ADSP -P EDA/L | D1EC 1/1 MC - G8 | --- | --- | --- | 14:30 |
| 15:30 | --- | --- | --- | --- | AKS, NK M2 EC Thesis FO&R/L | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | GPK | | | | | |
|---|--|--------------------------------|--------------------------------|--------------------------------|--------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | --- | --- | D4ECA1 SC_DE_III -L F105 | 08:30 |
| 09:30 | D3ECB DCS -L F107 | D3ECB DCS -L F107 | --- | D4ECA1 SC_DE_III -L F105 | --- | 09:30 |
| 10:30 | --- | --- | D3ECB DCS -L F107 | D4ECA1 MC - F105 | --- | 10:30 |
| 11:30 | --- | --- | --- | --- | --- | 11:30 |
| 12:30 | --- | --- | Minor Proj -P | --- | --- | 12:30 |
| 13:30 | D3ECB1 DCS -P COMM/L | D4ECA1 SC_DE_III -L F105 | D4ECA1 SC_DE_III -L F105 | --- | D3ECB1 DCS -T G7 | 13:30 |
| 14:30 | | | D3ECB2 DCS -T PROJ/L | --- | --- | 14:30 |
| 15:30 | NS, HK, GPK, TDS, SMM, GS D4ECA Mjr Proj F105 | D3ECB2 DCS -P COMM/L | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | CJ | | | | | |
|---|-----------------------------------|------------------------|--|------------------------|------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | D2ECA EMFT -L G6 | --- | D2ECB EMFT -L G7 | 08:30 |
| 09:30 | D2ECA EMFT -L G6 | D2ECB EMFT -L G7 | --- | D2ECB EMFT -L G7 | --- | 09:30 |
| 10:30 | D2ECB2 OOP&DS - P COMP/L | --- | --- | D4ECA2 MC - MW/L | --- | 10:30 |
| 11:30 | --- | D2ECA EMFT -L G6 | AKS, NK, CJ, DS, SK, PP D4ECA Mjr Proj F105 | --- | --- | 11:30 |
| 12:30 | --- | --- | --- | CJ, SNJ D3ECB1 | D3ECB2 | 12:30 |
| 13:30 | --- | --- | --- | MWR -P MW/L | MWR -P MW/L | 13:30 |
| 14:30 | D2ECB1 OOP&DS - P COMP/L | --- | --- | --- | --- | 14:30 |
| 15:30 | --- | --- | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | | KDS | | | | | |
|---|---|---------------------------------|---------------------------------|--------------------------------|-------------------------------|---------------|--|
| | | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | D3ECA1 CN_DE_II -L F106 | --- | D3ECB1 CN_DE_II -L COMP/L | --- | --- | 08:30 | |
| 09:30 | --- | --- | --- | D3ECA1 CN_DE_II -L EDC/L | D3ECB1 CN_DE_II -L MW/L | 09:30 | |
| 10:30 | --- | --- | --- | | --- | 10:30 | |
| 11:30 | NS, GJK, KDS, NM, HSG, HLK D4ECA Mjr Proj F105 | --- | --- | D2ECA2 M&C/L -P EDC/L | Minor Proj -P | 11:30 | |
| 12:30 | Minor Proj -P | D3ECB1 CN_DE_II -L COMP/L | D3ECB1 CN_DE_II -L COMP/L | --- | D3ECA1 CN_DE_II -L MP/L | 12:30 | |
| 13:30 | D3ECA1 CN_DE_II -L EDC/L | --- | --- | --- | D2ECA1 M&C/L -P EDC/L | 13:30 | |
| 14:30 | --- | D1EC 2/1 MC - COMP/L | --- | --- | | 14:30 | |
| 15:30 | --- | Minor Proj -P | --- | --- | --- | 15:30 | |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | | |

| | DS | | | | | |
|---|-------------------------------------|-------------------------------------|--|----------------------------------|----------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | D3ECA2 ITC_DE_II -L FO&R/L | --- | D3ECB2 ITC_DE_II -L FO&R/L | --- | --- | 08:30 |
| 09:30 | --- | NS, DS M2 EC Thesis FO&R/L | --- | D3ECA2 ITC_DE_II -L COMM/L | D3ECB2 ITC_DE_II -L EDC/L | 09:30 |
| 10:30 | --- | --- | Minor Proj -P | --- | --- | 10:30 |
| 11:30 | --- | --- | AKS, NK, CJ, DS, SK, PP D4ECA Mjr Proj F105 | --- | Minor Proj -P | 11:30 |
| 12:30 | --- | D3ECB2 ITC_DE_II -L PROJ/L | D3ECB2 ITC_DE_II -L FO&R/L | --- | D3ECA2 ITC_DE_II -L FO&R/L | 12:30 |
| 13:30 | D3ECA2 ITC_DE_II -L MW/L | D3ECB1 MC - TI/L | --- | --- | --- | 13:30 |
| 14:30 | --- | Minor Proj -P | --- | --- | --- | 14:30 |
| 15:30 | --- | --- | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | SK | | | | | |
|---|--------------------------|------------------------------------|--|----------------------------|---------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | D3ECB MWR -L F107 | --- | --- | D3ECB MWR -L F107 | --- | 08:30 |
| 09:30 | --- | --- | D3ECB MWR -L F107 | --- | D3ECA MWR -L F106 | 09:30 |
| 10:30 | --- | --- | --- | --- | --- | 10:30 |
| 11:30 | --- | Minor Proj -P | AKS, NK, CJ, DS, SK, PP D4ECA Mjr Proj F105 | --- | Minor Proj -P | 11:30 |
| 12:30 | --- | --- | --- | --- | --- | 12:30 |
| 13:30 | --- | D3ECB2 MC - FO&R/L | SK, PP D3ECA1 MWR -P MW/L | D3ECB2 MWR -T COMP/L | D3ECA2 MWR -T EDA/L | 13:30 |
| 14:30 | D3ECA1 MWR -T TI/L | SK, PP D3ECA2 MWR -P MW/L | | --- | D3ECB1 MWR -T MW/L | 14:30 |
| 15:30 | --- | --- | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | NM | | | | | |
|---|---|-----------------------------------|----------------------------------|-----------------------------------|----------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | D3ECB MCES -L F107 | --- | --- | D4ECA2 COA_DE_III -L EDC/L | 08:30 |
| 09:30 | --- | --- | --- | D4ECA2 COA_DE_III -L PROJ/L | --- | 09:30 |
| 10:30 | D3ECB MCES -L F107 | --- | --- | --- | D3ECB MCES -L F107 | 10:30 |
| 11:30 | NS, GJK, KDS, NM, HSG, HLK D4ECA Mjr Proj F105 | --- | --- | --- | --- | 11:30 |
| 12:30 | --- | --- | --- | Minor Proj -P | D3ECB1 MCES -T EDA/L | 12:30 |
| 13:30 | D3ECB2 MCES -P MP/L | D4ECA2 COA_DE_III -L COMP/L | D4ECA2 COA_DE_III -L EDC/L | --- | --- | 13:30 |
| 14:30 | | D3ECB1 MCES -P MP/L | --- | --- | D3ECB2 MCES -T FO&R/L | 14:30 |
| 15:30 | --- | --- | --- | Minor Proj -P | 15:30 | |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

| | TDS | | | | | |
|---|--|----------------------------|------------------------|----------------------------|-----------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | D2ECB LCS -L G7 | --- | --- | D2ECA LCS -L G6 | 08:30 |
| 09:30 | --- | --- | --- | --- | --- | 09:30 |
| 10:30 | D2ECA2 STRW/L -P | D2ECA LCS -L G6 | --- | --- | D2ECB LCS -L G7 | 10:30 |
| 11:30 | TI/L | Minor Proj -P | D2ECA2 LCS -T G7 | D1EC 3/1 MC - FO&R/L | --- | 11:30 |
| 12:30 | --- | --- | --- | Minor Proj -P | --- | 12:30 |
| 13:30 | D2ECB LCS -L G7 | --- | --- | D2ECA LCS -L G6 | --- | 13:30 |
| 14:30 | D2ECA1 LCS -T G6 | --- | D2ECB1 LCS -T G7 | --- | --- | 14:30 |
| 15:30 | NS, HK, GPK, TDS, SMM, GS D4ECA Mjr Proj F105 | D2ECB2 LCS -T PROJ/L | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | PP | | | | | |
|---|------------------------------------|---------------------------------|--|-------------------------------|-------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | D4ECA2 MC_DE_IV -L PROJ/L | --- | D3ECA MWR -L F106 | D3ECA MWR -L F106 | 08:30 |
| 09:30 | D4ECA2 MC_DE_IV -L COMP/L | --- | D4ECA2 MC_DE_IV -L MW/L | --- | --- | 09:30 |
| 10:30 | --- | --- | --- | --- | --- | 10:30 |
| 11:30 | --- | --- | AKS, NK, CJ, DS, SK, PP D4ECA Mjr Proj F105 | Minor Proj -P | --- | 11:30 |
| 12:30 | --- | Minor Proj -P | --- | --- | Minor Proj -P | 12:30 |
| 13:30 | --- | --- | SK, PP D3ECA1 MWR -P MW/L | D4ECA2 MC_DE_IV -L TI/L | --- | 13:30 |
| 14:30 | --- | SK, PP D3ECA2 | D2ECB1 | --- | --- | 14:30 |
| 15:30 | --- | MWR -P MW/L | MC - EDC/L | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | SMM | | | | | |
|---|--|----------------------------|----------------------------|----------------------------|---------------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | --- | D2ECB IM&DA -L G7 | D2ECA IM&DA -L G6 | --- | 08:30 |
| 09:30 | D2ECB IM&DA -L G7 | D2ECA IM&DA -L G6 | --- | --- | D2ECB IM&DA -L G7 | 09:30 |
| 10:30 | --- | --- | --- | --- | --- | 10:30 |
| 11:30 | Minor Proj -P | --- | --- | --- | --- | 11:30 |
| 12:30 | --- | Minor Proj -P | --- | D3ECA1 DCS -P COMM/L | Minor Proj -P | 12:30 |
| 13:30 | D2ECA IM&DA -L G6 | --- | D3ECA2 DCS -P COMM/L | --- | GJK, SMM D3ECA1 MCES -P MP/L | 13:30 |
| 14:30 | --- | D1EC 1/2 MC - FO&R/L | --- | --- | --- | 14:30 |
| 15:30 | NS, HK, GPK, TDS, SMM, GS D4ECA Mjr Proj F105 | --- | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | HSG | | | | | |
|---|---|-----------------------------|---------------------------------------|--------------------------------------|---------------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | M1 EC AWMC_PE_3 -L G8 | --- | --- | --- | M1 EC AWMC_PE_3 -L G8 | 08:30 |
| 09:30 | --- | --- | --- | --- | --- | 09:30 |
| 10:30 | --- | --- | --- | --- | --- | 10:30 |
| 11:30 | NS, GJK, KDS, NM, HSG, HLK D4ECA Mjr Proj F105 | D2ECB2 M&C/L -P EDC/L | D2ECB1 M&C/L -P EDC/L | D1EC 3/2 MC - G6 | --- | 11:30 |
| 12:30 | Minor Proj - P | M1 EC AOCS -P COMM/L | --- | --- | M1 EC AOCS -P COMM/L | 12:30 |
| 13:30 | --- | M1 EC AWMC_PE_3 -L G8 | --- | --- | M1 EC AOCS -P COMM/L | 13:30 |
| 14:30 | --- | --- | --- | --- | --- | 14:30 |
| 15:30 | D3_O Elect A NNFL_OE_A - L - G6 | --- | D3_O Elect A NNFL_OE_A - L - G7 | MR, HSG M2 EC Thesis FO&R/L | D3_O Elect A NNFL_OE_A - L - G6 | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | GS | | | | | |
|---|--|--------------------------|--------------------------|-------------------------------|--------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | D2ECA AC -L G6 | --- | --- | --- | --- | 08:30 |
| 09:30 | --- | --- | --- | D2ECA AC -L G6 | --- | 09:30 |
| 10:30 | D2ECB1 AC -P EDC/L | --- | D2ECA AC -L G6 | D2ECA1 OOP&DS -P COMP/L | D2ECA1 AC -P EDC/L | 10:30 |
| 11:30 | | --- | D2ECA1 AC -T MW/L | | | 11:30 |
| 12:30 | --- | --- | --- | --- | --- | 12:30 |
| 13:30 | --- | --- | Minor Proj -P | Minor Proj -P | --- | 13:30 |
| 14:30 | D2ECA2 AC -T PROJ/L | | --- | --- | --- | 14:30 |
| 15:30 | NS, HK, GPK, TDS, SMM, GS D4ECA Mjr Proj F105 | D2ECA2 AC -P EDC/L | D2ECB2 MC - COMP/L | --- | Minor Proj -P | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | HLK | | | | | |
|---|---|------------------------------|---------------------------------------|-----------------------------|---------------------------------------|--------------|
| | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | D2ECB OOP&DS -L G7 | --- | --- | --- | --- | 08:30 |
| 09:30 | --- | --- | D2ECB OOP&DS -L G7 | --- | --- | 09:30 |
| 10:30 | --- | | | | | 10:30 |
| 11:30 | NS, GJK, KDS, NM, HSG, HLK D4ECA Mjr Proj F105 | D4ECA2 VLSID -P COMP/L | D2ECB2 ISP/L -P EDA/L | D2ECB1 ISP/L -P EDA/L | D4ECA1 VLSID -P COMP/L | 11:30 |
| 12:30 | --- | --- | --- | --- | --- | 12:30 |
| 13:30 | --- | --- | --- | D2ECB OOP&DS -L G7 | D2ECA2 OOP&DS -P COMP/L | 13:30 |
| 14:30 | --- | --- | --- | --- | | 14:30 |
| 15:30 | D3_O Elect B NNFL_OE_B - L - G7 | Minor Proj -P | D3_O Elect B NNFL_OE_B - L - G6 | --- | D3_O Elect B NNFL_OE_B - L - G7 | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | |

Time-Table(Jan-May,2020) DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING, GNDEC, LUDHIANA w.e.f 06-01-2020

| | | SNJ | | | | | |
|---|--------------------------|----------------------------|--------------------------|--------------------------|--------------------------|---------------|--------------|
| | | Monday | Tuesday | Wednesday | Thursday | Friday | |
| 08:30 | --- | D2ECA OOP&DS -L G6 | --- | D2ECB AC -L G7 | --- | --- | 08:30 |
| 09:30 | --- | --- | D2ECA OOP&DS -L G6 | --- | D2ECA OOP&DS -L G6 | --- | 09:30 |
| 10:30 | --- | D2ECB1 | --- | D2ECB2 | --- | --- | 10:30 |
| 11:30 | --- | STRW/L -P TI/L | --- | STRW/L -P PROJ/L | D2ECB AC -L G7 | --- | 11:30 |
| 12:30 | --- | --- | --- | CJ, SNJ | --- | --- | 12:30 |
| 13:30 | Minor Proj -P | D2ECB AC -L G7 | --- | D3ECB1 MWR -P MW/L | --- | --- | 13:30 |
| 14:30 | D2ECB2 AC -P EDC/L | D1EC 2/2 MC - PROJ/L | D2ECB2 AC -T EDC/L | --- | --- | --- | 14:30 |
| 15:30 | --- | D2ECB1 AC -T G6 | --- | --- | --- | --- | 15:30 |
| Timetable generated with FET 5.34.3 on 1/3/20 5:08 PM | | | | | | | |