			AKS		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30	-X-	-X-	-X-	-X-	-X-
09:30	-X-	-X-	-X-	-X-	-X-
	M2EC		D3ECA	D3ECA	D3ECA
10:30	Thesis P		DCS L	DCS L	DCS L
	FO&R/L		F105	F105	F105
	AKS, NSG,				
	GJK				There's D
11:30	D4EC				Thesis P
	Major P				FO&R/L
	PROJ/L				
12:30	-X-	-X-	-X-	-X-	-X-
			AKS, MR, HK		
01.20	ALC CDL		D4EC		
01:30	,to, o	D3ECA1	Major P	M1EC	
	D3ECA2	DCS_Lab P	PROJ/L	Proj P	
	DCS_Lab P	COMM/L	D4EC4	FO&R/L	
02:30	COMM/L		MC -		
			MP/L		
03:30					
	Timetable	generated	with FET 6.1.2	on 4/12/23	10:09 AM

			NSG		
	Monday	Tuesday	Wednesday	Thursday	Friday
	D2ECB				
08:30	AC L				
	G7				
					M2EC
09:30					Thesis P
					FO&R/L
10:30					
	AKS, NSG,				
	GJK	D2ECB1			
11:30	D4EC	AC T			
	Major P	G6			
	PROJ/L				
	Thesis P	D3ECA2			
12:30	FO&R/L	MC -			
	ΤΟΩΤΊ	G7			
			D2ECB	D2ECB	D2ECB2
01:30			AC L	AC L	AC T
			G7	G7	G8
	NSG, HKA,				
	SMM				
02:30	D4EC			-X-	
	Major P				
	PROJ/L				
03:30				-X-	
	Timetable	generated	with FET 6.1.2	on 4/12/23	10:09 AM

			MR					
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30					D3ECB MWR L F104			
09:30	M1EC AMWE-E4 L G8			M2EC Thesis P FO&R/L	D3ECB2 MWR_Lab F			
10:30		M1EC Proj P	D3ECB MWR L F104	D3ECB MWR L F104	MWR/L			
11:30	Thesis P FO&R/L	FO&R/L						
12:30		D3ECB2 MC - MWR/L						
01:30	-X-	-x-	AKS, MR, HK D4EC Major P PROJ/L	-X-	-x-			
02:30	-X-	-X-	-X-	-X-	-X-			
03:30			M1EC AMWE-E4 L G8		M1EC AMWE-E4 L G8			
	Timetable generated with FET 6.1.2 on 4/12/23 10:09 AM							

			ВК		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30		Thesis P FO&R/L	D3ECA2 MCN-E2 L F105		
09:30	D3ECA2 MCN-E2 L F105			M1EC AOCS L G8	M1EC AOCS L G8
10:30	D3ECA2 MCN-E2 T F105	D3ECA2 MCN-E2 L F105	M1EC AOCS L G8		
11:30				M2EC Thesis P FO&R/L	
12:30					
01:30	M1EC AOCS-Lab	M1EC AOCS-Lab P	 D4EC5		D4EC1 Seminar P
02:30	P FO&R/L	FO&R/L	MC - COMM/L		MP/L
03:30	 Timetable	generated v	vith FET 6.1.2	 on 4/12/23	10:09 AM

			НК		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30					
09:30			D4EC1 Sat_C-E4 L G8	D4EC1 Sat_C-E4 L F104	
10:30	D4EC1 Sat_C-E4 L G6	D2ECA1 MC_Lab P EDC/L		D4EC1 Sat_C-E4 T G8	
11:30		EDC/L			
12:30	D3_OE1 BECE-OE L G7		D3_OE1 BECE-OE L G7	D3_OE1 BECE-OE L G6	
01:30			AKS, MR, HK D4EC Major P PROJ/L	HK, DS D4EC Major P PROJ/L	D4EC2 Seminar P
02:30			D4EC6 MC - G6		G7
03:30					
	Timetable	generated	with FET 6.1.2	on 4/12/23	10:09 AM

			GJK		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30	D3ECA1 ARM-E1 L G8			D3ECA1	
09:30	D4EC1 Soft_C-E3 L G6	D3ECA1 ARM-E1 L G8		ARM-Lab P MP/L	D4EC1 Soft_C-E3 I G6
10:30			D4EC1 Soft_C-E3 L G7		D4EC1 Soft_C-E3 7 G7
11:30	AKS, NSG, GJK D4EC Major P PROJ/L				
12:30					D3ECA1 ARM-E1 T G8
01:30		D4EC1	D3ECA1 ARM-E1 L G8		
02:30		Soft_C-Lab P EDA/L			D1EC1 MC - TI/L
03:30		Minor P	Minor P		
	Timetable	e generated w	ith FET 6.1.2	on 4/12/23	10:09 AM

			NK		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30					
	D4EC2				D4EC2
09:30	IoT-E3 L	Minor P			IoT-E3 L
	COMP/L				COMP/L
	M1EC		D4EC2		D4EC2
10:30	ADSP L		IoT-E3 L	N 41 F.C	IoT-E3 T
	G8		COMP/L	M1EC	MP/L
				ADSP-Lab P	M1EC
11:30				EDA/L	ADSP L
					G8
12:30					
01:30		D4FC2			N 41 F.C
		D4EC2 IoT-Lab P	D4EC1		M1EC
02:30		TI/L	MC -		ADSP-Lab P EDA/L
		1 1/ L	F105		EDA/L
				M1EC	
03:30		Minor P		ADSP L	
				G8	
	Timetable	generated	with FET 6.1.2	2 on 4/12/23	10:09 AM

			GPK		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30		D3ECB DCS L F104	D3ECB2 D_VLSI-E2 L F104		
09:30	D3ECB2 D_VLSI-E2 L F104		D3ECB DCS L F104		D3ECB1
10:30	D3ECB2 D_VLSI-E2 T F104	D3ECB2 D_VLSI-E2 L F104			DCS_Lab COMM/L
11:30					
12:30		D3ECA1 MC - G8			
01:30	AKS, GPK			D3ECB DCS L F104	D3ECB2
02:30	D3ECA2 DCS_Lab P COMM/L			GPK, HKA, PP D4EC Major P PROJ/L	DCS_Lab I COMM/L
03:30					M2EC Thesis P FO&R/L
	Timetable	e generated	with FET 6.1.	2 on 4/12/23 1	L0:09 AM

			CJ		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30	-X-	D2ECB EMFT L G7			D2ECA2
09:30	D2ECA1 STRW_Lab	D2ECA EMFT L G6			STRW_Lab F EDA/L
10:30	P EDA/L			D2ECA EMFT L G6	
11:30	D2ECB EMFT L G7		D2ECA EMFT L G6		
12:30		D3ECB1 MC - TI/L			
01:30	D2ECB1 STRW_Lab	D3ECB Minor P F104		D3ECA Minor P F105	D3ECA Minor P F105
02:30	P EDA/L			D2ECB EMFT L G7	
03:30					
	Timetable	generated	with FET 6.1.2	on 4/12/23	3 10:09 AM

	KDS							
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30	D3ECA2 OC-E1 L F105			D3ECA2 OC-Lab P				
09:30		D3ECA2 OC-E1 L F105		COMM/L				
10:30			D2ECB1 MC_Lab P		M1EC ADSD-E3 L G8			
11:30			EDC/L					
12:30					D3ECA2 OC-E1 T F105			
01:30	D2ECB2		D3ECA2 OC-E1 L F105					
02:30	MC_Lab P EDC/L		D4EC3 MC - EDC/L	D3ECA, D3ECB TNP				
03:30	M1EC ADSD-E3 L G8	M1EC ADSD-E3 L G8		D3ECA, D3ECB TNP				
	Timetabl	e generated	d with FET 6.1	2 on 4/12/23 1	0:09 AM			

	HKA						
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30				D2ECA OOP&DS L G6			
09:30			D2ECA OOP&DS L G6	D2ECB2			
10:30	D2ECB OOP&DS L G7	D2ECA2 OOP_Lab P		OOP_Lab P COMP/L			
11:30		COMP/L			D2ECA OOP&DS L G6		
12:30	Minor P	D3ECA3 MC - COMP/L					
01:30		D2ECB OOP&DS L G7			D2ECB1		
02:30	NSG, HKA, SMM D4EC Major P PROJ/L		D2ECB OOP&DS L G7	GPK, HKA, PP D4EC Major P PROJ/L			
03:30							
	Timetable	e generated	with FET 6.1.	2 on 4/12/23	10:09 AM		

			DS		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30			D2ECB LCS L G7	D2ECB LCS L G7	
09:30	D2ECA2				D2ECB LCS L G7
10:30	MC_Lab P EDC/L	D2ECB1 LCS T G7			
11:30				D2ECA1 MC - COMP/L	
12:30					
01:30	DS, PP, SMM D4EC Major P PROJ/L		D2ECA1 OOP_Lab P	HK, DS D4EC Major P PROJ/L	
02:30			COMP/L	-X-	D2ECB2 LCS T G6
03:30				-X-	
	Timetable	generated	with FET 6.1.2	on 4/12/23	10:09 AM

	SK					
	Monday	Tuesday	Wednesday	Thursday	Friday	
08:30	-X-	D3ECA MWR L F105	D3ECA1 MMSP-E2 L MP/L			
09:30	D3ECA1 MMSP-E2 L MP/L		D3ECA MWR L F105		D3ECA MWR L F105	
10:30	D3ECA1 MMSP-E2 T MP/L	D3ECA1 MMSP-E2 L MP/L				
11:30				D2ECA2 MC - MP/L		
12:30				Minor P		
01:30	D3ECA1	D3ECA2			D3ECB1	
02:30	MWR_Lab P MWR/L	MWR_Lab P MWR/L			MWR_Lab P MWR/L	
03:30		Minor P				
	Timetable generated with FET 6.1.2 on 4/12/23 10:09 AM					

	PP					
	Monday	Tuesday	Wednesday	Thursday	Friday	
			D2ECA			
08:30			IMDA L			
			G6			
	D2ECB	D2ECB	D2ECB	D2ECA		
09:30	IMDA L	IMDA L	IMDA L	IMDA L		
	G7	G7	G7	G6		
10:30		Darcha			DOECD1	
	D2ECA	ISP_Lab P EDA/L		D2ECB3	D2ECB1	
11:30	IMDA L			MC -	ISP_Lab P EDA/L	
	G6			MWR/L		
12:30						
	DS, PP,					
	SMM					
01:30	D4EC					
	Major P		D2ECA2			
	PROJ/L		ISP_Lab P			
			EDA/L	GPK, HKA, PP	D1EC2	
02:30				D4EC	MC -	
02:30				Major P		
			PROJ/L	EDC/L		
03:30						
	Timetable	e generated	with FET 6.1.	2 on 4/12/23	10:09 AM	

	HSG					
	Monday	Tuesday	Wednesday	Thursday	Friday	
	D3ECB1					
08:30	CS-E1 L			DOCE 1		
	F104			D3ECB1		
		D3ECB1		CS-Lab P		
09:30		CS-E1 L		EDA/L		
		F104				
10:30						
				D2ECA3		
11:30				MC -		
				F104		
				M2EC	D3ECB1	
12:30				Thesis P	CS-E1 T	
				FO&R/L	F104	
			D3ECB1			
01:30			CS-E1 L			
			F104			
02:30						
03:30						
	Timetable	generated	with FET 6.1.2	on 4/12/23	10:09 AM	

	SMM					
	Monday	Tuesday	Wednesday	Thursday	Friday	
	D3ECB2		MBA			
08:30	RISC-E1 L		E-WASTE L	Darcha		
	TI/L		MBA	D3ECB2		
		D3ECB2		RISC-Lab P		
09:30		RISC-E1 L		TI/L		
		TI/L				
	MBA					
10:30	E-WASTE L					
	MBA					
11:30						
					D3ECB2	
12:30					RISC-E1 T	
					TI/L	
	DS, PP,					
	SMM		D3ECB2		MBA	
01:30	D4EC	Minor P	RISC-E1 L		E-WASTE I	
	Major P		TI/L	D2ECA1	MBA	
	PROJ/L			ISP_Lab P		
	NSG, HKA,			EDA/L		
	SMM		D4EC2		D1EC3	
02:30	D4EC		MC -		MC -	
	Major P		G8		G8	
	PROJ/L					
03:30						
	Timetable	generated	with FET 6.1.2 on 4/12/23 10:09 AM			

	KPS					
	Monday	Tuesday	Wednesday	Thursday	Friday	
		D2ECA	D3ECB1			
08:30	-X-	AC L	VLSI_P-E2 L			
		G6	MWR/L		D2ECA1	
	D3ECB1				AC_Lab P	
00.20	VLSI_P-E2				EDC/L	
09:30	L			D05604		
	MWR/L			D2ECB1		
	D3ECB1	D 3 E 6 D 4	DOEGA.	AC_Lab P	Data	
	VLSI_P-E2	D3ECB1	D2ECA	EDC/L	D2ECA	
10:30	T	VLSI_P-E2 L	AC L		AC L	
	MWR/L	MWR/L	G6		G6	
				D2ECB1		
11:30				MC -		
				F105		
12:30						
	D2ECA2					
01:30	AC T					
	G8					
	D3ECB	D2ECA1			D1EC4	
02:30	Minor P	AC T		Minor P	MC -	
	F104	G7			F104	
03:30						
	Timetable	generated v	with FET 6.1.2	on 4/12/23	10:09 AM	

	RKG					
	Monday	Tuesday	Wednesday	Thursday	Friday	
08:30						
09:30			D4EC2 MEMS-E4 L COMP/L	D4EC2 MEMS-E4 L F105		
10:30	D4EC2 MEMS-E4 L COMP/L		D2ECB2 STRW_Lab P	D4EC2 MEMS-E4 T G7	D2ECB2 AC_Lab P	
11:30			EDA/L	D2ECB2 MC - G6	EDC/L	
12:30						
01:30	D2ECA1 LCS T G6	D2ECA LCS L G6		D2ECA2	D2ECA LCS L G6	
02:30	D2ECA LCS L G6	D2ECA2 LCS T G8		AC_Lab P EDC/L	D1EC5 MC - F105	
03:30						
	Timetable generated with FET 6.1.2 on 4/12/23 10:09 AM					