## Guru Nanak Dev Engineering College, Ludhiana

## **Department of Electronics and Communication Engineering**

## Time-Table for M.Tech. (Part-Time) w.e.f. 29-08-2020

DAYLINE CLASSES.

Day	Time	5th Semester
Saturday	9:00 am - 10:30 am	Open elective: Disaster Reduction and Management (CE)
	11:00 am - 12:30 pm	VLSI Design Automation (GPK)
	2:00 pm - 4:00 pm	Pre-Thesis Project (HSG)
Sunday	9:00 am - 10:30 am	Open elective: Disaster Reduction and Management (CE)
	11:00 am - 12:30 pm	VLSI Design Automation (GPK)
	2:00 pm - 3:00 pm	Pre-Thesis Project (HSG)
	3:00 pm - 4:00 pm	Pre-Thesis Seminar (NM)

GPK: Prof. Gurpurneet Kaur; NM: Prof. Naazia Makkar;

HSG: Dr. Harsimranjit Singh Gill; CE: Faculty from Civil Deptt. for open elective

Online Room: F-105 ber ECE

Programme Coordinator

M.Tech. ECE

necta Seehra 28/8/2020

CC

- 1 Dean Academics
- 2 Academic Incharge, ECE
- 3 Prof. Harminder Kaur-I for uploading on website