

GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Time Table for B.Tech (2nd year LEET classes in Blended mode)

Time	Monday	Tuesday	Wednesday	Thursday	Friday
9:00-10:00	L-Signals and Systems (NK)	L-Digital Electronics (KDS)	L-Engineering Mathematics – III (Ms. Rajbir Kaur)	L-Computer Architecture (GS)	L-Enviornmental Science (SMM)
10:00-11:00	L-Network Analysis and Synthesis (CJ)	L-Computer Architecture (GS)	L-Electronic Devices (HKA)	L-Engineering Mathematics – III (Ms. Rajbir Kaur)	L-Computer Architecture (GS)
11:00-12:00	L-Engineering Mathematics – III (Ms. Rajbir Kaur)	L-Signals and Systems (NK)	L-Digital Electronics (KDS)	L-Signals and Systems (NK)	L-Digital Electronics (KDS)
12:00-13:00	*	*	*	*	*
13:00-14:00	P- Digital Electronics (KDS)	L-Electronic Devices (HKA)	L-Network Analysis and Synthesis (CJ)	L-Electronic Devices (HKA)	P- Electronic Devices (GS)
14:00-15:00		L-Enviornmental Science (SMM)	MC D2EC3 BBB: Room G8(HSG)	L-Network Analysis and Synthesis (CJ)	
15:00-16:00		T-Network Analysis and Synthesis (CJ)	T-Signals and Systems (NK)	T-Digital Electronics (KDS)	

Teachers: Ms. Rajbir Kaur , NK- Dr. Navneet Kaur, CJ- Pf. Chahat Jain, KDS- Pf. Kuldeepak Singh, HKA-Pf. Harminder Kaur Aulakh, , SMM- Pf. Shivmanmeet Singh, HSG- Dr. Harsimranjit Singh Gill , GS- Pf. Gurinder Singh

Note: Classes in Blended mode to be held in Computatinal Lab