

	D1E C	D2ECA	D2ECB	D3ECA	D3ECB	D4ECA	M1EC	M2EC	D2CE	D4WC	
M o n d a y	08:30	---	NAS L CJ G6	ED L HKA G7	AWP L SK F-105	MP&MC L GJK F-106	IS/RC ---	-x-	---	-x-	---
	09:30	---	D2ECA1 D2ECA2 NAS T DE T CJ NSG TI/L G8	CA L X G7	IS/RC ---	CN L KDS F-106	D4ECA1 D4ECA2 MOB COMP(ELECT-IV) L L P VLSI D(ELECT-IV) L BK Y MP/L MW/L	M COMM SYS-LAB P MR FO&R/L	---	---	---
	10:30	---	SS L NK G6	D2ECB1 D2ECB2 ED-LAB P DE-LAB P HKA HK EDC/L MW/L	DSP L DS F-105	ACS L GPK F-106	IS/RC ---		MMC(ELECT-5) L AKS TI/L	---	---
	11:30	---	EVS L Y G6	D2ECB1 D2ECB2 ED-LAB P DE-LAB P HKA HK EDC/L MW/L	-x-	-x-	IS/RC ---	CC & NC(ELECT-2) L BK G8	-x-	---	---
	12:30	---	-x-	-x-	ACS L AKS F-105	D3ECB1 D3ECB2 ACS-LAB P CN-LAB P GPK DS COMM/L COMP/L	-x-	-x-	---	---	---
	1:30	---	EM-III L SS(AP) G6	DE L NSG G7	MP&MC L GJK F-105	D3ECB1 D3ECB2 ACS-LAB P CN-LAB P GPK DS COMM/L COMP/L	IS/RC ---	Soft Comp (ELECT-1) L Y G8	OE (IND SFTY) L Dr.HPS(M&P) --	D2CEB Basic of E & A (CE) L SK G-1	---
	2:30	---	ED L HKA G6	IS/RC ---	D3ECA1 D3ECA2 ACS-LAB P CN-LAB P AKS KDS COMM/L COMP/L	D3ECB1 D3ECB2 MP&MC-LAB P DSP-LAB P CJ NK MP/L EDA/L	MAJ PROJ P BK, HK, SK, X PROJ/L	MCS L MR G8	---	---	---
	3:30	---	IS/RC ---	EVS L Y G7	D3ECA1 D3ECA2 ACS-LAB P CN-LAB P AKS KDS COMM/L COMP/L	D3ECB1 D3ECB2 MP&MC-LAB P DSP-LAB P CJ NK MP/L EDA/L	OE-II L OE-II_FACULTY ----	-x-	---	---	WC(OE-II) L HK G6
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	D1EC	D2ECA	D2ECB	D3ECA	D3ECB	D4ECA	M1EC	M2EC	D2CE	D4WC	
T u e s d a y	08:30	---	DE L NSG G6	SS L NK G7	MP&MC L GJK F-105	ACS L GPK F-106	IS/RC ---	R & IPR L HK G8	---	-x-	---
	09:30	---	CA L X G6	D2ECB1 D2ECB2 NAS T ED-LAB P CJ Y TI/L EDC/L	IS/RC ---	DSP L DS F-106	MAJ PROJ P MR, NK, KDS PROJ/L	---	---	D2CEA Basic of E & A (CE) L SK F-103	---
	10:30	---	NAS L CJ G6	D2ECB1 D2ECB2 DE T ED-LAB P NSG Y G8 EDC/L	ACS L AKS F-105	AWP L MR F-106	IS/RC ---	VLSI PHY D & A-LAB P GPK FO&R/L	---	---	---
	11:30	---	EM-III L SS(AP) G6	IS/RC ---	-x-	-x-	D4ECA1 D4ECA2 ON (ELECT-III) L A MOSFET BS(ELECT-III) L HKA X COMP/L COMM/L		-x-	D2CEB Basic of E & A (CE) L SK G-1	---
	12:30	---	-x-	-x-	D3ECA1 D3ECA2 CN-LAB P MP&MC-LAB P KDS GJK COMP/L MP/L	IS/RC ---	-x-	-x-	---	---	---
	1:30	---	DE L NSG G6	NAS L CJ G7	D3ECA1 D3ECA2 CN-LAB P MP&MC-LAB P KDS GJK COMP/L MP/L	D3ECB1 D3ECB2 D3ECB3 MC P MC P MC P Z BK X FO&R/L EDC/L F-105	IS/RC ---	AUD -1 L Dr. SK(CSE) G18 (MBA)	---	---	---
	2:30	---	EVS L Y G6	EM-III L SS(AP) G7	DSP L DS F-105	D3ECB1 D3ECB2 DSP-LAB P ACS-LAB P NK GPK EDA/L COMM/L	IS/RC ---	CC & NC(ELECT-2) L BK G8	MMC(ELECT-5) L AKS TI/L	---	---
	3:30	DD DDD 11111 EEEEEE CCCCC 12345 MMMMM CCCCC PPPPP DSXYZ SK C OMP MP P/L /L L	IS/RC ---	D2ECB1 D2ECB2 MC P MC P CJ MR EDC/L G6	CN L KDS F-105	D3ECB1 D3ECB2 DSP-LAB P ACS-LAB P NK GPK EDA/L COMM/L	D4ECA1 D4ECA2 MC P MC P HK GJK G8 G7	-x-	---	---	---

	D1E C	D2ECA	D2ECB	D3ECA	D3ECB	D4ECA	M1EC	M2EC	D2CE	D4WC	
W e d n e s d a y	08:30	---	ED L HKA G6	CA L X G7	DSP L DS F-105	CN L KDS F-106	IS/RC ---	CC & NC(ELECT-2) L BK G8	---	-x-	---
	09:30	---	EM-III L SS(AP) G6	DE L NSG G7	D3ECA1 D3ECA2 MP&MC T DSP T GJK DS TI/L PROJ/L	AWP L MR F-106	D4ECA1 D4ECA2 MOB COMP(ELECT-IV) T L P VLSI D(ELECT-IV) T BK Y MP/L MW/L	VLSI Phy Design and Auto L GPK G8	---	D2CEA Basic of E & A (CE) L SK G-1	---
	10:30	---	D2ECA1 D2ECA2 D2ECA3 MC P MC P MC P GPK AKS HKA MW/L TI/L PROJ/L	SS L NK G7	IS/RC ---	MP&MC L GJK F-106	D4ECA1 D4ECA2 ON-LAB(ELECT-III) P A MOSFET-LAB(ELECT-III) P HK X FO&R/L COMP/L	Soft Comp (ELECT-1) L Y G8	---	---	---
	11:30	---	NAS L CJ G6	IS/RC ---	-x-	-x-	D4ECA1 D4ECA2 ON-LAB(ELECT-III) P A MOSFET-LAB(ELECT-III) P HK X FO&R/L COMP/L	MCS L MR G8	-x-	---	---
	12:30	---	-x-	-x-	AWP L SK F-105	IS/RC ---	-x-	-x-	MMC(ELECT-5) L AKS TI/L	---	---
	1:30	---	D2ECA1 D2ECA2 DE T ED-LAB P NSG HKA G8 EDC/L	IS/RC ---	D3ECA1 D3ECA2 DSP T MP&MC T DS GJK PROJ/L TI/L	COI/OB/IS L SJK(AP)/ Dr.NK(MBA) G6	D4ECA1 D4ECA2 MOB COMP(ELECT-IV) L L P VLSI D(ELECT-IV) L BK Y MP/L MW/L	VLSI PHY D & A-LAB P GPK FO&R/L	OE (IND SFTY) L Dr.HPS(M&P) --	---	---
	2:30	---	D2ECA1 D2ECA2 SS T ED-LAB P NK HKA PROJ/L EDC/L	NAS L CJ G7	D3ECA1 D3ECA2 DSP-LAB P ACS-LAB P SK AKS EDA/L COMM/L	COI/OB/IS L SJK(AP)/ Dr.NK(MBA) G6	IS/RC ---		---	---	---
	3:30	---	IS/RC ---	EM-III L SS(AP) G7	D3ECA1 D3ECA2 DSP-LAB P ACS-LAB P SK AKS EDA/L COMM/L	D3ECB1 D3ECB2 DSP T MP&MC T DS GJK PROJ/L TI/L	OE-II L OE-II_FACULTY ----	-x-	---	---	WC(OE-II) L HK G6

	D1E C	D2ECA	D2ECB	D3ECA	D3ECB	D4ECA	M1EC	M2EC	D2CE	D4WC	
T h u r s d a y	08:30	---	CA L X G6	NAS L CJ G7	AWP L SK F-105	AWP L MR F-106	IS/RC ---	-x-	---	-x-	---
	09:30	---	SS L NK G6	DE L NSG G7	CN L KDS F-105	ACS L GPK F-106	MAJ PROJ P GJK, DS, Y PROJ/L	R & IPR L HK G8	---	---	---
	10:30	---	D2ECA1 D2ECA2 ED-LAB P DE-LAB P HKA BK EDC/L MW/L	CA L X G7	ACS L AKS F-105	DSP L DS F-106	IS/RC ---	M COMM SYS-LAB P MR FO&R/L	---	---	---
	11:30	---	D2ECA1 D2ECA2 ED-LAB P DE-LAB P HKA BK EDC/L MW/L	IS/RC ---	-x-	-x-	MAJ PROJ P AKS, HK, X PROJ/L		-x-	D2CEB Basic of E & A (CE) L SK G-2	---
	12:30	---	-x-	-x-	MP&MC L GJK F-105	IS/RC ---	-x-	-x-	---	---	---
	1:30	---	IS/RC ---	ED L HKA G7	COI/OB/IS L SJK(AP)/ Dr.NK(MBA) G6	D3ECB1 D3ECB2 CN-LAB P DSP T KDS DS COMP/L PROJ/L	D4ECA1 D4ECA2 MOB COMP(ELECT-IV) L L P VLSI D(ELECT-IV) L BK Y MP/L MW/L	AUD -1 L Dr. SK(CSE) G18 (MBA)	---	D2CEA Basic of E & A (CE) L SK G-1	---
	2:30	---	IS/RC ---	SS L NK G7	COI/OB/IS L SJK(AP)/ Dr.NK(MBA) G6	D3ECB1 D3ECB2 CN-LAB P MP&MC-LAB P KDS CJ COMP/L MP/L	MAJ PROJ P BK, HK, SK PROJ/L	VLSI Phy Design and Auto L GPK G8	PRE-THESIS P MR FO&R/L	---	---
	3:30	---	IS/RC ---	EM-III L SS(AP) G7	D3ECA1 D3ECA2 D3ECA3 MC P MC P MC P NK Y KDS G8 EDC/L PROJ/L	D3ECB1 D3ECB2 MP&MC T MP&MC-LAB P GJK CJ TI/L MP/L	D4ECA1 D4ECA2 ON (ELECT-III) L A MOSFET BS(ELECT-III) L HKA X COMP/L COMM/L	-x-		---	---
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	D1E C	D2ECA	D2ECB	D3ECA	D3ECB	D4ECA	M1EC	M2EC	D2CE	D4WC	
Friday	08:30	---	SS L NK G6	EVS L Y G7	CN L KDS F-105	DSP L DS F-106	IS/RC ---	-x-	PRE-THESIS P BK FO&R/L	-x-	---
	09:30	---	CA L X G6	D2ECB1 D2ECB2 DE-LAB P DE T HK NSG MW/L G8	IS/RC ---	MP&MC L GJK F-106	MAJ PROJ P GPK, CJ, HKA PROJ/L	---		---	---
	10:30	---	ED L HKA G6	D2ECB1 D2ECB2 DE-LAB P SS T HK NK MW/L PROJ/L	IS/RC ---	CN L KDS F-106	D4ECA1 D4ECA2 SRTECE-LAB P SRTECE-LAB P AKS MR MP/L F-105	VLSI Phy Design and Auto L GPK G8	---	---	---
	11:30	---	DE L NSG G6	IS/RC ---	-x-	-x-	D4ECA1 D4ECA2 SRTECE-LAB P SRTECE-LAB P AKS MR MP/L F-105	Soft Comp (ELECT-1) L Y G8	-x-	---	---
	12:30	---	-x-	-x-	D3ECA1 D3ECA2 MP&MC-LAB P DSP-LAB P GJK SK MP/L EDA/L	IS/RC ---	-x-	-x-	---	---	---
	1:30	---	D2ECA1 D2ECA2 DE-LAB P SS T BK NK MW/L PROJ/L	ED L HKA G7	D3ECA1 D3ECA2 MP&MC-LAB P DSP-LAB P GJK SK MP/L EDA/L	IS/RC ---	D4ECA1 D4ECA2 ON (ELECT-III) T A MOSFET BS(ELECT-III) T KDS X COMP/L COMM/L	R & IPR L HK G8	OE (IND SFTY) L Dr.HPS(M&P) --	---	---
	2:30	---	D2ECA1 D2ECA2 DE-LAB P NAS T BK CJ MW/L TI/L	IS/RC ---	D3ECA, D3ECB T & P DS	D3ECA, D3ECB T & P DS	D4ECA1 D4ECA2 ON (ELECT-III) L A MOSFET BS(ELECT-III) L KDS X COMP/L COMM/L	MCS L MR G8	---	---	---
	3:30	---	IS/RC ---	D2ECB1 D2ECB2 SS T NAS T NK CJ PROJ/L TI/L			OE-II L OE-II_FACULTY ----	-x-	---	---	WC(OE-II) L HK G6
Timetable generated with FET 5.49.0 on 9/5/21 4:04 PM											