

	D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC
M o n d a y	08:30	---	D2 EC A AC L NSG G6 D2 EC B1 M&C-Lab PSTRW-Lab P RKG EDC/L D2 EC B2 STRW-Lab P NK EDA/L	D3 EC A1 OC_E-I L KDS F-105 D3 EC A2 CS_E-I L HSG Comm/L D3 EC B1 ARM_EI L RISC_EI L GJK SMM TI/L D3 EC B2 F-105 Comp/L	-x-	-x- ---
	09:30	---	D2 EC A OOP&DS L HKA G6 D2 EC B1 M&C-Lab P RKG EDC/L D2 EC B2 STRW-Lab P NK EDA/L	D3 EC A1 MW-Lab P SK MW/L D3 EC A2 PDCS-Lab P GPK Comm/L D3 EC B MWR L MR F-105	-x-	AOCS-Lab P BK FO&R/L ---
	10:30	---	D2 EC A IM&DA PP G6 D2 EC B LOOP&DS L HKA G7	D3 EC A1 MW-Lab P SK MW/L D3 EC A2 PDCS-Lab P GPK Comm/L D3 EC B Min pro P CJ Proj/L	D4 EC 1 Sof Comp_EIII- Lab P HSG EDA/L D4 EC 2 VLSI(HDL)_EIII- Lab P GJK Comp/L	---
	11:30	---	D2 EC A EMFT L CJ G6 D2 EC B LCS L DS G7	-x-	D4 EC 1 Sof Comp_EIII- Lab P HSG EDA/L D4 EC 2 VLSI(HDL)_EIII- Lab P GJK Comp/L	Pedagogy L AK(CSE) MBA Semi_hall ---
	12:30	---	-x-	D3 EC A, D3 EC B OE-I L OE_Y	-x-	-x- THESIS P BK FO&R/L
	13:30	---	D2 EC A1 AC T NSG Comm/L D2 EC A2 AC-Lab P KPS EDC/L D2 EC B EMFT L CJ G7	D3 EC A DCS L AKS F-105 D3 EC B Min pro P Proj/L	D4 EC 1 Soft Comp_EIII L GPK MW/L D4 EC 2 VLSI(HDL)_EIII L GJK EDA/L	ADSP L NK G8 ---
	14:30	---	D2 EC A1 LCS T DS Comm/L D2 EC A2 AC-Lab P KPS EDC/L D2 EC B PIM&DA L PP G7	D3 EC A1 MC P NK MW/L D3 EC A2 MC P SMM G8 D3 EC A3 MC P KDS Comp /L D3 EC B1 MC P AKS TI/L D3 EC B2 MC P BK MP/L D3 EC B3 MC P HSG F-105	D4 EC A Maj Proj P NSG, MR, HK Proj/L	ADSP-Lab P SK EDA/L ---
15:30	---	D2 EC A, D2 EC B Sk_E L	D3 EC A, D3 EC B Sk_E L	D4 EC A Sk_E L	---	

		D1 EC	D2 EC				D3 EC				D4 EC	M1 EC	M2 EC	
T u e s d a y	08:30	D D D D D 1 1 1 1 1 E E E E E C C C C C 1 2 3 4 5 M M M M M C C C C C P P P P P D S H K R S K S P K G S G C O M E m P D G T I p / L C 8 / L / L / L					D3 EC A1 D3 EC A2 D3 EC B DCS-Lab PMW-Lab P MWR L HK CJ MR Comm/L MW/L F-105				-x-	-x-	---	
	09:30	---	D2 EC A D2 EC B LCS L AC L DS KPS G6 G7				D3 EC A1 D3 EC A2 D3 EC B DCS-Lab PMW-Lab P DCS L HK CJ GPK Comm/L MW/L F-105				D4 EC 1 D4 EC 2 Seminar P Seminar P NSG NK G8 Proj/L	ADSP-Lab P SK EDA/L	---	
	10:30	---	D2 EC A D2 EC B1 D2 EC B2 IM&DA LOOP&DS-Lab PM&C-Lab P PP HKA RKG G6 Comp/L EDC/L	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_E-I L CS_E-I L ARM_EI L RISC_EI L KDS HSG GJK SMM F-105 G7 TI/L Comm/L				D4 EC 1 D4 EC 2 Seminar P Seminar P NSG NK G8 Proj/L		---	---			
	11:30	---	D2 EC B1 D2 EC B2 OOP&DS-Lab PM&C-Lab P HKA RKG Comp/L EDC/L					-x-				D4 EC A OE-II L OE_Z	AOCS L BK G8	---
	12:30	---	-x-				D3 EC A, D3 EC B OE-I L OE_Y				-x-	-x-	THESIS P MR FO&R/L	
	13:30	---	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 M&C-Lab P AC T STRW-Lab P OOP&DS- Lab P SMM NSG NK HKA EDC/L G7 EDA/L Comp/L	D3 EC A D3 EC B1 D3 EC B2 Min pro PMW-Lab P DCS-Lab P CJ GPK Proj/L MW/L Comm/L				D4 EC A, D4 EC B MC P HK, GJK, PP TI/L		AWMC_ELEC-3 L AKS G8	---			
	14:30	---	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 M&C-Lab P LCS T STRW-Lab P OOP&DS- Lab P SMM DS NK HKA EDC/L G6 EDA/L Comp/L	D3 EC A D3 EC B1 D3 EC B2 MWR L MW-Lab P DCS-Lab P SK CJ GPK F-105 MW/L Comm/L				D4 EC A Maj Proj P AKS, NSG, GJK Proj/L		AME_ELEC-4 L MR G8	---			
	15:30	---	D2 EC A, D2 EC B Sk_E L				D3 EC A, D3 EC B Sk_E L				D4 EC A Sk_E L	---	---	
Timetable generated with FET 6.1.3 on 3/24/22 9:43 PM														

	D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC	
W e d n e s d a y	08:30	D1ECA1 PPS lab P Comp/L	D2 EC A1 D2 EC A2 D2 EC B STRW-Lab PM&C-Lab POOP&DS L NK SMM HKA EDA/L EDC/L G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 MCN_E-II MMSP_E-II VLSI Digital_VL L L PhyEII L SI_EII L BK HSG KPS RKG G6 MP/L TI/L F-105	-x-	-x- ---	
	09:30	D1ECA1 PPS lab P Comp/L	D2 EC A1 D2 EC A2 D2 EC B STRW-Lab PM&C-Lab PIM&DA L NK SMM PP EDA/L EDC/L G7	D3 EC A D3 EC B1 D3 EC B2 MWR L DCS-Lab PMW-Lab P SK GPK CJ F-105 Comm/L MW/L	-x-	PROJECT P MR FO&R/L ---	
	10:30	---	D2 EC A1 D2 EC A2 D2 EC B ISP-Lab POOP&DS-Lab P AC L PP HKA KPS EDA/L Comp/L G7	D3 EC A D3 EC B1 D3 EC B2 DCS L DCS-Lab PMW-Lab P AKS GPK CJ F-105 Comm/L MW/L	D4 EC 1 D4 EC 2 MEMS_EIV LSC_EIV L RKG HK G6 G8	---	
	11:30	---	D2 EC A1 D2 EC A2 D2 EC B ISP-Lab POOP&DS-Lab P LCS L PP HKA DS EDA/L Comp/L G7	-x-	D4 EC A OE-II L OE_Z	Pedagogy L AK(CSE) MBA Semi_hall ---	
	12:30	---	-x-	D3 EC A, D3 EC B OE-I L OE_Y	-x-	-x- ---	
	13:30	D1ECB PPS lab P Comp/L	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 AC-Lab PSTRW-Lab P LCS T AC T NSG NK DS KPS EDC/L EDA/L MP/L F-105	D3 EC A, D3 EC B T&P P KDS	D4 EC A Maj Proj P HKA, SK, HSG Proj/L	AOCS L BK G8	THESIS P AKS FO&R/L
	14:30	D1ECB PPS lab P Comp/L	D2 EC A1 D2 EC A2 AC-Lab PSTRW-Lab P NSG NK EDC/L EDA/L		D4 EC A Maj Proj P PP, SMM, KPS, RKG Proj/L	AME_ELEC-4 L MR G8	---
15:30	---	D2 EC A, D2 EC B Sk_E L	D3 EC A, D3 EC B Sk_E L	D4 EC A Sk_E L	---	THESIS P GJK FO&R/L	

		D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC
T h u r s d a y	08:30	---	D2 EC A D2 EC B IM&DA L AC L PP KPS G6 G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_EI- CS_EI- ARM_EI- RISC_EI- Lab P Lab P Lab P Lab P KDS HSG GJK SMM Comm/L EDA/L MP/L TI/L	-x-	-x-	---
	09:30	---	D2 EC A D2 EC B EMFT L OOP&DS L CJ HKA G6 G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_EI- CS_EI- ARM_EI- RISC_EI- Lab P Lab P Lab P Lab P KDS HSG GJK SMM Comm/L EDA/L MP/L TI/L	-x-	AOCS-Lab P BK FO&R/L	---
	10:30	---	D2 EC A D2 EC B1 D2 EC B2 AC L AC-Lab PISP-Lab P NSG KPS PP G6 EDC/L EDA/L	D3 EC A D3 EC B Min pro P DCS L CJ GPK Proj/L F-105	D4 EC 1 D4 EC 2 MEMS_EIV LSC_EIV L RKG HK G8 TI/L		---
	11:30	---	D2 EC A D2 EC B1 D2 EC B2 LCS L AC-Lab PISP-Lab P DS KPS PP G6 EDC/L EDA/L	-x-	D4 EC 1 D4 EC 2 Soft Comp_EIII TVLSI(HDL)_EIII T GPK GJK MW/L F-105	ADSP L NK G8	---
	12:30	---	-x-	D3 EC A, D3 EC B OE-I L OE_Y	-x-	-x-	---
	13:30	---	D2 EC A1 D2 EC A2 D2 EC B OOP&DS-Lab PISP-Lab P LCS L HKA PP DS Comp/L EDA/L G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 MCN_E-II MMSP_E- VLSI Digital_V L II L PhyEII L LSI_EII L BK HSG KPS RKG TI/L MP/L G6 F-105	D4 EC A Maj Proj P GPK, CJ, KDS Proj/L	AWMC_ELEC-3 L AKS G8	THESIS P HK FO&R/L
	14:30	---	D2 EC A1 D2 EC A2 D2 EC B OOP&DS-Lab PISP-Lab P EMFT L HKA PP CJ Comp/L EDA/L G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 MCN_E-II MMSP_E- VLSI Digital_V T II T PhyEII T LSI_EII T BK HSG KPS RKG G8 Comm/L F-105 MW/L	D4 EC 1 D4 EC 2 Soft Comp_EIII LVLSI(HDL)_EIII L GPK GJK Proj/L G6	PROJECT P MR FO&R/L	---
15:30	---	D2 EC A, D2 EC B Sk_E L	D3 EC A, D3 EC B Sk_E L	D4 EC A Sk_E L		---	

		D1 EC	D2 EC			D3 EC				D4 EC		M1 EC	M2 EC	
Friday	08:30	D1ECA2 PPS lab P Comp/L	D2 EC A D2 EC B LCS L EMFT L DS CJ G6 G7			D3 EC A D3 EC B MWR L DCS L SK GPK F-105 Proj/L				-x-		-x-	---	
	09:30	D1ECA2 PPS lab P Comp/L	D2 EC A D2 EC B1 D2 EC B2 OOP&DS LISP-Lab PAC-Lab P HKA PP KPS G6 EDA/L EDC/L			D3 EC A1	D3 EC A2	D3 EC B1	D3 EC B2	D4 EC 1 D4 EC 2 MEMS_EIV L SC_EIV L RKG HK MW/L G7		---	---	
	10:30	---	D2 EC A D2 EC B1 D2 EC B2 EMFT L ISP-Lab PAC-Lab P CJ PP KPS G6 EDA/L EDC/L			D3 EC A D3 EC B DCS L MWR L AKS MR F-105 G7				D4 EC 1 D4 EC 2 MEMS_EIV T SC_EIV T RKG HK Comp/L Comm/L		ADSP L NK G8	---	
	11:30	---	D2 EC A1	D2 EC A2	D2 EC A3	D2 EC B1	D2 EC B2	-x-				D4 EC A OE-II L OE_Z	AOCS L BK G8	---
	12:30	---	-x-			D3 EC A, D3 EC B OE-I L OE_Y				-x-		-x-	---	
	13:30	---	D2 EC A AC L NSG G6			D3 EC A1	D3 EC A2	D3 EC B1	D3 EC B2	D4 EC 1 D4 EC 2 Soft Comp_EIII L VLSI(HDL)_EIII L GPK GJK MP/L Proj/L		---	---	
	14:30	---	D2 EC B1 D2 EC B2 AC T LCS T KPS DS F-105 Comm/L			D3 EC A1	D3 EC A2	D3 EC B1	D3 EC B2	D4 EC A Maj Proj P BK, HK, NK Proj/L		AWMC_ELEC-3 L AKS G8	---	
	15:30	---	D2 EC A, D2 EC B Sk_E L			D3 EC A, D3 EC B Sk_E L				D4 EC A Sk_E L		AME_ELEC-4 L MR G8	THESIS P NSG FO&R/L	