

D2 EC A					
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30	OOP&DS HKA G6	D2 EC A1 D2 EC A2 M&C-Lab OOP&DS-Lab SMM HKA EDC/L COMP/L	LCS DS G6	IM&DA PP G6	-x-
09:30	EMFT CJ G6	D2 EC A1 D2 EC A2 M&C-Lab OOP&DS-Lab SMM HKA EDC/L COMP/L	IM&DA PP G6	OOP&DS HKA G6	AC NSG G6
10:30	AC NSG G6	D2 EC A1 D2 EC A2 ISP-Lab AC-Lab PP KPS EDA/L EDC/L	OOP&DS HKA G6	D2 EC A1 D2 EC A2 AC-Lab STRW-Lab NSG NK EDC/L EDA/L	LCS DS G6
11:30	LCS DS G6	D2 EC A1 D2 EC A2 ISP-Lab AC-Lab PP KPS EDA/L EDC/L	AC NSG G6	D2 EC A1 D2 EC A2 AC-Lab STRW-Lab NSG NK EDC/L EDA/L	EMFT CJ G6
12:30	-x-	-x-	-x-	-x-	-x-
13:30	IM&DA PP G6	D2 EC A1 D2 EC A2 AC ISP-Lab NSG PP MP/L EDA/L	D2 EC A1 D2 EC A2 OOP&DS-Lab M&C-Lab HKA SMM COMP/L EDC/L	EMFT CJ G6	D2 EC A1 D2 EC A2 STRW-Lab AC NK NSG EDA/L MP/L
14:30	D2 D2 D2 EC EC EC A1 A2 A3 M M M C C C GP NS HK K G A FO M &R G7 P/L /L	D2 EC A1 D2 EC A2 LCS ISP-Lab DS PP G8 EDA/L	D2 EC A1 D2 EC A2 OOP&DS-Lab M&C-Lab HKA SMM COMP/L EDC/L	-x-	D2 EC A1 D2 EC A2 STRW-Lab LCS NK DS EDA/L PROJ/L
15:30	-x-	-x-	-x-	-x-	-x-
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		<b>D2 EC B</b>				
		<b>Monday</b>	<b>Tuesday</b>	<b>Wednesday</b>	<b>Thursday</b>	<b>Friday</b>
<b>08:30</b>	AC KPS G7	IM&DA PP G7	D2 EC B1 D2 EC B2 AC-Lab STRW-Lab KPS NK EDC/L EDA/L	-x-	EMFT CJ G7	
<b>09:30</b>	IM&DA PP G7	LCS DS G7	D2 EC B1 D2 EC B2 AC-Lab STRW-Lab KPS NK EDC/L EDA/L	LCS DS G7	IM&DA PP G7	
<b>10:30</b>	OOP&DS HKA G7	EMFT CJ G7	D2 EC B1 D2 EC B2 M&C-Lab ISP-Lab X PP EDC/L EDA/L	AC KPS G7	D2 EC B1 D2 EC B2 STRW-Lab OOP&DS-Lab NK HKA EDA/L COMP/L	
<b>11:30</b>	EMFT CJ G7	OOP&DS HKA G7	D2 EC B1 D2 EC B2 M&C-Lab ISP-Lab X PP EDC/L EDA/L	OOP&DS HKA G7	D2 EC B1 D2 EC B2 STRW-Lab OOP&DS-Lab NK HKA EDA/L COMP/L	
<b>12:30</b>	-x-	-x-	-x-	-x-	-x-	
<b>13:30</b>	LCS DS G7	D2 EC B1 D2 EC B2 OOP&DS-Lab AC-Lab HKA KPS COMP/L EDC/L	AC KPS G7	D2 EC B1 D2 EC B2 ISP-Lab LCS PP DS EDA/L FO&R/L	D2 EC B1 D2 EC B2 LCS M&C-Lab DS X FO&R/L EDC/L	
<b>14:30</b>	D2 D2 EC EC B1 B2 MC MC CJ MR EDC/ L G8	D2 EC B1 D2 EC B2 OOP&DS-Lab AC-Lab HKA KPS COMP/L EDC/L	-x-	D2 EC B1 D2 EC B2 ISP-Lab AC PP KPS EDA/L COMM/L	D2 EC B1 D2 EC B2 AC M&C-Lab KPS X G8 EDC/L	
<b>15:30</b>	-x-	-x-	-x-	-x-	-x-	
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		<b>D3 EC A</b>				
		<b>Monday</b>	<b>Tuesday</b>	<b>Wednesday</b>	<b>Thursday</b>	<b>Friday</b>
<b>08:30</b>	D3 D3 EC EC A1 A2 OC_ECS_E LECT LECT -I- -I- Lab Lab KDS HSG COM EDA M/L /L	MINOR PROJECT CJ PROJ/L	D3 EC A1 D3 EC A2 OC_ELECT-I CS_ELECT-I KDS HSG F-106 MP/L	MWR SK F-105	D3 EC A1 D3 EC A2 MCN_ELEC-II MSP_ELEC-II BK HSG PROJ/L COMM/L	
<b>09:30</b>	D3 D3 EC EC A1 A2 OC_ECS_E LECT LECT -I- -I- Lab Lab KDS HSG COM EDA M/L /L	D3 EC A1 MCN_ELEC-II BK EDA/L	MWR SK F-105	D3 EC A1 D3 EC A2 MWR-Lab DCS-Lab SK GPK MWR/L COMM/L	D3 EC A1 D3 EC A2 DCS-Lab MWR-Lab HK CJ COMM/L MWR/L	
<b>10:30</b>	DCS AKS F-105	D3 EC A1 D3 EC A2 OC_ELECT-I CS_ELECT-I KDS HSG F-106 MP/L	D3 EC A1 D3 EC A2 OC_ELECT-I CS_ELECT-I KDS HSG COMM/L G8	D3 EC A1 D3 EC A2 MWR-Lab DCS-Lab SK GPK MWR/L COMM/L	D3 EC A1 D3 EC A2 DCS-Lab MWR-Lab HK CJ COMM/L MWR/L	
<b>11:30</b>	-x-	-x-	-x-	-x-	-x-	
<b>12:30</b>	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	
<b>13:30</b>	D3 D3 EC EC A1 A2 MC MSP N_EL_ELE EC-II C-II BK HSG EDC/ L G8	D3 EC A2 MSP_ELEC-II HSG PROJ/L	D3 EC A, D3 EC B T&P KDS	D3 EC A1 D3 EC A2 MCN_ELEC-II MSP_ELEC-II BK HSG G8 G7	DCS AKS F-105	
<b>14:30</b>	D3 D3 D3 EC EC EC A1 A2 A3 M M M C C C NK SM KD M S CO PR M F-1 OJ M/ 05 /L L	DCS AKS F-105	D3 EC A1 D3 EC A2 OC_ELECT-I CS_ELECT-I KDS HSG MP/L G7	MWR SK F-105		
<b>15:30</b>	D3 EC A, D3 EC B Skill Enhancem ent	D3 EC A, D3 EC B Skill Enhancement	D3 EC A, D3 EC B Skill Enhancement	D3 EC A, D3 EC B Skill Enhancement	D3 EC A, D3 EC B Skill Enhancement	
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D3 EC B					
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30	D3 D3 EC EC B1 B2 ARM RISC _ELE _ELE CT-I- CT-I- Lab Lab GJK SM MP/ M L TI/L	MWR MR F-106	D3 EC B1 D3 EC B2 ARM_ELECT-I RISC_ELECT-I GJK SMM MWR/L COMM/L	DCS GPK F-106	D3 EC B1 D3 EC B2 VLSI Phy_ELECT-II Digital VLSI_ELECT-II KPS X MP/L F-105
09:30	D3 D3 EC EC B1 B2 ARM RISC _ELE _ELE CT-I- CT-I- Lab Lab GJK SM MP/ M L TI/L	D3 EC B1 D3 EC B2 VLSI Phy_ELECT-II Digital KPS X PROJ/L MWR/L	MWR MR F-106	---	DCS GPK F-106
10:30	DCS GPK F-106	D3 EC B1 D3 EC B2 ARM_ELECT-I RISC_ELECT-I GJK SMM G6 PROJ/L	D3 EC B1 D3 EC B2 ARM_ELECT-I RISC_ELECT-I GJK SMM MP/L G7	MINOR PROJECT CJ PROJ/L	MWR MR F-106
11:30	-x-	-x-	-x-	-x-	-x-
12:30	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y	D3 EC A, D3 EC B OE-I OE_Y
13:30	D3 D3 EC EC B1 B2 VLSI Digital Phy_ al ELEC VLSI T-II _ELE CT-II KPS X MW TI/L R/L	D3 EC B1 D3 EC B2 MWR-Lab DCS-Lab CJ GPK MWR/L COMM/L	D3 EC A, D3 EC B T&P KDS	D3 EC B1 D3 EC B2 VLSI Phy_ELECT-II Digital VLSI_ELECT-II KPS X EDC/L TI/L	D3 EC B1 D3 EC B2 DCS-Lab MWR-Lab GPK CJ COMM/L MWR/L
14:30	D3 D3 D3 EC EC EC B1 B2 B3 M M M C C C AK BK PP S M G6 W TI/L R/L	D3 EC B1 D3 EC B2 MWR-Lab DCS-Lab CJ GPK MWR/L COMM/L		D3 EC B1 D3 EC B2 ARM_ELECT-I RISC_ELECT-I GJK SMM PROJ/L MWR/L	D3 EC B1 D3 EC B2 DCS-Lab MWR-Lab GPK CJ COMM/L MWR/L
15:30	D3 EC A, D3 EC B Skill Enhancem ent	D3 EC A, D3 EC B Skill Enhancement	D3 EC A, D3 EC B Skill Enhancement	D3 EC A, D3 EC B Skill Enhancement	D3 EC A, D3 EC B Skill Enhancement

<b>D4 EC A</b>					
	<b>Monday</b>	<b>Tuesday</b>	<b>Wednesday</b>	<b>Thursday</b>	<b>Friday</b>
<b>08:30</b>	-x-	D4 EC 1 D4 EC 2 SEMINAR SEMINAR NSG NK G8 TI/L	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK PROJ/L TI/L	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK COMP/L TI/L	-x-
<b>09:30</b>	D4 EC MAJOR PROJECT NSG, MR, HK PROJ/L	D4 EC 1 D4 EC 2 SEMINAR SEMINAR NSG NK G8 TI/L	D4 EC MAJOR PROJECT GPK, CJ, KDS PROJ/L	D4 EC MAJOR PROJECT PP, SMM, KPS, X PROJ/L	D4 EC MAJOR PROJECT HKA, SK, HSG PROJ/L
<b>10:30</b>	D4 D4 EC 1 EC 2 Soft VLSI( Com HDL) p_EL _ELE ECT- CT- III- III- Lab Lab HSG GJK EDA CO /L MP/ L	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK COMP/L TI/L	D4 EC MAJOR PROJECT BK, HK, NK PROJ/L	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK FO&R/L G8	D4 EC MAJOR PROJECT AKS, NSG, GJK PROJ/L
<b>11:30</b>	D4 D4 EC 1 EC 2 Soft VLSI( Com HDL) p_EL _ELE ECT- CT- III- III- Lab Lab HSG GJK EDA CO /L MP/ L	OE-II OE_Z	OE-II OE_Z	D4 EC 1 D4 EC 2 Soft Comp_ELECT-III VLSI(HDL)_ELECT-III GPK GJK FO&R/L G6	OE-II OE_Z
<b>12:30</b>	-x-	-x-	-x-	-x-	-x-
<b>13:30</b>	D4 D4 EC 1 EC 2 Soft VLSI( Com HDL) p_EL _ELE ECT- CT- III III GPK GJK CO MP/ MM/ L	D4 EC A , D4 EC B MC HK, GJK G7	D4 EC 1 D4 EC 2 Soft Comp_ELECT-III VLSI(HDL)_ELECT-III GPK GJK F-105 TI/L	D4 EC 1 D4 EC 2 Soft Comp_ELECT-III VLSI(HDL)_ELECT-III GPK GJK F-105 MP/L	-x-
<b>14:30</b>	-x-	-x-	-x-	-x-	-x-
<b>15:30</b>	-x-	-x-	-x-	-x-	-x-
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<b>M1 EC Automatic Group</b>					
	<b>Monday</b>	<b>Tuesday</b>	<b>Wednesday</b>	<b>Thursday</b>	<b>Friday</b>
<b>08:30</b>	-x-	-x-	-x-	-x-	-x-
<b>09:30</b>	M1 EC AOCS BK G8	M1 EC PROJECT MR FO&R/L	M1 EC AOCS BK G8	---	M1 EC AOCS-Lab BK FO&R/L
<b>10:30</b>	M1 EC AME_ELEC -4 MR G8		---	---	
<b>11:30</b>	M1 EC Pedagogy AK(CSE) MBA Semi_hall	M1 EC AWMC_ELEC-3 AKS G8	M1 EC Pedagogy AK(CSE) MBA Semi_hall	M1 EC AWMC_ELEC-3 AKS G8	M1 EC AWMC_ELEC-3 AKS G8
<b>12:30</b>	-x-	-x-	-x-	-x-	-x-
<b>13:30</b>	M1 EC ADSP-Lab SK EDA/L	M1 EC AOCS-Lab BK FO&R/L	M1 EC ADSP NK G8	---	M1 EC AOCS BK G8
<b>14:30</b>			M1 EC ADSP-Lab SK EDA/L	M1 EC AME_ELEC-4 MR G8	M1 EC PROJECT MR FO&R/L
<b>15:30</b>	M1 EC ADSP NK G8	M1 EC AME_ELEC-4 MR G8	M1 EC ADSP NK G8		
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	<b>M2 EC Automatic Group</b>				
	<b>Monday</b>	<b>Tuesday</b>	<b>Wednesday</b>	<b>Thursday</b>	<b>Friday</b>
<b>08:30</b>	---	---	---	---	---
<b>09:30</b>	---	---	---	M2 EC THESIS MR FO&R/L	---
<b>10:30</b>	---	---	M2 EC THESIS AKS FO&R/L	---	---
<b>11:30</b>	---	M2 EC THESIS BK FO&R/L	---	---	M2 EC THESIS NSG FO&R/L
<b>12:30</b>	---	---	---	---	---
<b>13:30</b>	---	---	M2 EC THESIS HK FO&R/L	---	---
<b>14:30</b>	---	---	M2 EC THESIS GJK FO&R/L	---	---
<b>15:30</b>	---	---	---	---	---
Timetable generated with FET 6.1.3 on 1/30/22 5:30 PM					