

	D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC	
M o n d a y	08:30	---	D2 EC A D2 EC B OOP&DS AC HKA KPS G6 G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_ELEC CS_ELEC ARM_ELE RISC_ELE T-I-Lab T-I-Lab CT-I-Lab CT-I-Lab KDS HSG GJK SMM COMM/L EDA/L MP/L TI/L	-x-	-x-	---
	09:30	---	D2 EC A D2 EC B EMFT IM&DA CJ PP G6 G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_ELEC CS_ELEC ARM_ELE RISC_ELE T-I-Lab T-I-Lab CT-I-Lab CT-I-Lab KDS HSG GJK SMM COMM/L EDA/L MP/L TI/L	MAJOR PROJECT NSG, MR, HK PROJ/L	AOCS BK G8	---
	10:30	---	D2 EC A D2 EC B AC OOP&DS NSG HKA G6 G7	D3 EC A D3 EC B DCS DCS AKS GPK F-105 F-106	D4 EC 1 D4 EC 2 Soft VLSI(HDL)_ELE Comp_ELECT- VLSI(HDL)_ELE III-Lab CT-III-Lab HSG GJK EDA/L COMP/L	AME_ELEC-4 MR G8	---
	11:30	---	D2 EC A D2 EC B LCS EMFT DS CJ G6 G7	-x-	D4 EC 1 D4 EC 2 Soft VLSI(HDL)_ELE Comp_ELECT- VLSI(HDL)_ELE III-Lab CT-III-Lab HSG GJK EDA/L COMP/L	Pedagogy AK(CSE) MBA Semi_hall	---
	12:30	D1ECA2 PPS lab T(PPS) COMP/L	-x-	D3 EC A, D3 EC B OE-I OE_Y	-x-	-x-	---
	13:30	D1ECA2 PPS lab T(PPS) COMP/L	D2 EC A D2 EC B IM&DA LCS PP DS G6 G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 MCN_ELE MSP_ELE VLSI Digital C-II C-II Phy_ELEC VLSI_ELE T-II CT-II BK HSG KPS X EDC/L G8 MWR/L TI/L	D4 EC 1 D4 EC 2 Soft VLSI(HDL)_ELE Comp_ELECT- VLSI(HDL)_ELE III CT-III GPK GJK COMM/L MP/L	ADSP-Lab SK EDA/L	---
	14:30	---	D2 EC D2 EC D2 EC D2 EC D2 EC A1 A2 A3 B1 B2 MC MC MC MC MC GPK NSG HKA CJ MR FO&R/ L G7 MP/L EDC/L G8	D3 EC D3 EC D3 EC D3 EC D3 EC D3 EC A1 A2 A3 B1 B2 B3 MC MC MC MC MC MC NK SMM KDS AKS BK PP COM F-105 PROJ/ G6 MWR/ M/L L L TI/L	-x-	---	---
15:30	---	-x-	D3 EC A, D3 EC B Skill Enhancement	-x-	ADSP NK G8	---	

	D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC	
Tuesday	08:30 D D D D D 1 1 1 1 1 E E E E E C C C C C 1 2 3 4 5 M M M M M C C C C C D S H K S K S X P G S C M F O W O M F- M R & P 1 M R / L 0 / L / L 5		D2 EC A1 D2 EC A2 D2 EC B M&C-Lab OOP&DS-Lab IM&DA SMM HKA PP EDC/L COMP/L G7	D3 EC A D3 EC B MINOR PROJECT MWR CJ MR PROJ/L F-106	D4 EC 1 D4 EC 2 SEMINAR SEMINAR NSG NK G8 TI/L	-x-	---
	09:30	---	D2 EC A1 D2 EC A2 D2 EC B M&C-Lab OOP&DS-Lab LCS SMM HKA DS EDC/L COMP/L G7	D3 EC A1 D3 EC B1 D3 EC B2 MCN_ELEC- VLSI Digital II Phy_ELECT- VLSI_ELECT- BK KPS X EDA/L PROJ/L MWR/L	D4 EC 1 D4 EC 2 SEMINAR SEMINAR NSG NK G8 TI/L	PROJECT MR FO&R/L	---
	10:30	---	D2 EC A1 D2 EC A2 D2 EC B ISP-Lab AC-Lab EMFT PP KPS CJ EDA/L EDC/L G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_ELEC CS_ELEC ARM_ELE RISC_ELE T-I T-I CT-I CT-I KDS HSG GJK SMM F-106 MP/L G6 PROJ/L	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK COMP/L TI/L		---
	11:30	---	D2 EC A1 D2 EC A2 D2 EC B ISP-Lab AC-Lab OOP&DS PP KPS HKA EDA/L EDC/L G7	-x-	D4 EC A OE-II OE_Z	AWMC_ELEC-3 AKS G8	THESIS BK FO&R/L
	12:30	---	-x-	D3 EC A, D3 EC B OE-I OE_Y	-x-	-x-	---
	13:30	---	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 AC ISP-Lab OOP&DS AC-Lab NSG PP HKA KPS MP/L EDA/L COMP/L EDC/L	D3 EC A2 D3 EC B1 D3 EC B2 MSP_ELEC-II MWR-Lab DCS-Lab HSG CJ GPK PROJ/L MWR/L COMM/L	D4 EC A, D4 EC B MC HK, GJK G7	AOCS-Lab BK FO&R/L	---
	14:30	---	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 LCS ISP-Lab OOP&DS AC-Lab DS PP HKA KPS G8 EDA/L COMP/L EDC/L	D3 EC A D3 EC B1 D3 EC B2 DCS MWR-Lab DCS-Lab AKS CJ GPK F-105 MWR/L COMM/L	-x-		---
	15:30	---	-x-	D3 EC A, D3 EC B Skill Enhancement	-x-	AME_ELEC-4 MR G8	---

		D1 EC	D2 EC			D3 EC				D4 EC		M1 EC	M2 EC
W e d n e s d a y	08:30	---	D2 EC A LCS DS G6	D2 EC B1 AC-Lab KPS EDC/L	D2 EC B2 STRW-Lab NK EDA/L	D3 EC A1 OC_ELEC T-I KDS F-106	D3 EC A2 CS_ELECT -I HSG MP/L	D3 EC B1 ARM_ELE CT-I GJK MWR/L	D3 EC B2 RISC_ELE CT-I SMM COMM/L	D4 EC 1 MEMS_ELECT-IV X PROJ/L	D4 EC 2 SC_ELECT-IV HK TI/L	-x-	---
	09:30	---	D2 EC A IM&DA PP G6	D2 EC B1 AC-Lab KPS EDC/L	D2 EC B2 STRW-Lab NK EDA/L	D3 EC A D3 EC B MWR MWR SK MR F-105 F-106				MAJOR PROJECT GPK, CJ, KDS PROJ/L		AOCS BK G8	---
	10:30	---	D2 EC A OOP&DS HKA G6	D2 EC B1 M&C-Lab X EDC/L	D2 EC B2 ISP-Lab PP EDA/L	D3 EC A1 OC_ELEC T-I KDS COMM/L	D3 EC A2 CS_ELEC T-I HSG G8	D3 EC B1 ARM_ELE CT-I GJK MP/L	D3 EC B2 RISC_ELE CT-I SMM G7	MAJOR PROJECT BK, HK, NK PROJ/L		---	THESIS AKS FO&R/L
	11:30	---	D2 EC A AC NSG G6	D2 EC B1 M&C-Lab X EDC/L	D2 EC B2 ISP-Lab PP EDA/L	-x-				D4 EC A OE-II OE_Z		Pedagogy AK(CSE) MBA Semi_hall	---
	12:30	---	-x-			D3 EC A, D3 EC B OE-I OE_Y				-x-		-x-	---
	13:30	---	D2 EC A1 OOP&DS-Lab HKA COMP/L	D2 EC A2 M&C-Lab SMM EDC/L	D2 EC B AC KPS G7	D3 EC A, D3 EC B T&P KDS				D4 EC 1 Soft Comp_ELECT-III GPK F-105	D4 EC 2 VLSI(HDL)_ELECT -III GJK TI/L	ADSP NK G8	THESIS HK FO&R/L
	14:30	---	D2 EC A1 OOP&DS-Lab HKA COMP/L	D2 EC A2 M&C-Lab SMM EDC/L	-x-					ADSP-Lab SK EDA/L		THESIS GJK FO&R/L	
	15:30	---	-x-			D3 EC A, D3 EC B Skill Enhancement				-x-		---	---
Timetable generated with FET 6.1.3 on 1/30/22 5:56 PM													

		D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC
T h u r s d a y	08:30	---	D2 EC A IM&DA PP G6	D3 EC A D3 EC B MWR DCS SK GPK F-105 F-106	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK COMP/L TI/L	-x-	---
	09:30	D1ECA1 PPS lab T(PPS) COMP/L	D2 EC A D2 EC B OOP&DS LCS HKA DS G6 G7	D3 EC A1 D3 EC A2 MWR-Lab DCS-Lab SK GPK MWR/L COMM/L	MAJOR PROJECT PP, SMM, KPS, X PROJ/L	---	THESIS MR FO&R/L
	10:30	D1ECA1 PPS lab T(PPS) COMP/L	D2 EC A1 D2 EC A2 D2 EC B AC-Lab STRW-Lab AC NSG NK KPS EDC/L EDA/L G7	D3 EC A1 D3 EC A2 D3 EC B MWR-Lab DCS-Lab MINOR PROJECT SK GPK CJ MWR/L COMM/L PROJ/L	D4 EC 1 D4 EC 2 MEMS_ELECT-IV SC_ELECT-IV X HK FO&R/L G8	---	---
	11:30	---	D2 EC A1 D2 EC A2 D2 EC B AC-Lab STRW-Lab OOP&DS NSG NK HKA EDC/L EDA/L G7	-x-	D4 EC 1 D4 EC 2 Soft VLSI(HDL)_ELECT- Comp_ELECT-III III GPK GJK FO&R/L G6	AWMC_ELEC-3 AKS G8	---
	12:30	---	-x-	D3 EC A, D3 EC B OE-I OE_Y	-x-	-x-	---
	13:30	---	D2 EC A D2 EC B1 D2 EC B2 EMFT ISP-Lab LCS CJ PP DS G6 EDA/L FO&R/L	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 MCN_ELE MSP_ELEC VLSI Digital C-II -II Phy_ELEC VLSI_ELE T-II CT-II BK HSG KPS X G8 G7 EDC/L TI/L	D4 EC 1 D4 EC 2 Soft VLSI(HDL)_ELECT- Comp_ELECT-III III GPK GJK F-105 MP/L	---	---
	14:30	---	D2 EC B1 D2 EC B2 ISP-Lab AC PP KPS EDA/L COMM/L	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 OC_ELECT CS_ELECT ARM_ELE RISC_ELE -I -I CT-I CT-I KDS HSG GJK SMM MP/L G7 PROJ/L MWR/L	-x-	AME_ELEC-4 MR G8	---
15:30	---	-x-	D3 EC A, D3 EC B Skill Enhancement	-x-	ADSP NK G8	---	

	D1 EC	D2 EC	D3 EC	D4 EC	M1 EC	M2 EC	
Friday	08:30	---	D2 EC B EMFT CJ G7	D3 EC A1 D3 EC A2 D3 EC B1 D3 EC B2 MCN_ELEC-II MSP_ELEC-II VLSI Phy_ELECT-II Digital VLSI_ELECT-II BK HSG KPS X PROJ/L COMM/L MP/L F-105	-x-	-x-	---
	09:30	---	D2 EC A D2 EC B AC IM&DA NSG PP G6 G7	D3 EC A1 D3 EC A2 D3 EC B DCS-Lab MWR-Lab DCS HK CJ GPK COMM/L MWR/L F-106	MAJOR PROJECT HKA, SK, HSG PROJ/L	AOCS-Lab BK FO&R/L	---
	10:30	---	D2 EC A D2 EC B1 D2 EC B2 LCS STRW-Lab OOP&DS-Lab DS NK HKA G6 EDA/L COMP/L	D3 EC A1 D3 EC A2 D3 EC B DCS-Lab MWR-Lab MWR HK CJ MR COMM/L MWR/L F-106	MAJOR PROJECT AKS, NSG, GJK PROJ/L	---	---
	11:30	---	D2 EC A D2 EC B1 D2 EC B2 EMFT STRW-Lab OOP&DS-Lab CJ NK HKA G6 EDA/L COMP/L	-x-	D4 EC A OE-II OE_Z	AWMC_ELEC-3 AKS G8	THESIS NSG FO&R/L
	12:30	---	-x-	D3 EC A, D3 EC B OE-I OE_Y	-x-	-x-	---
	13:30	D1ECB PPS lab T(PPS) COMP/L	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 STRW-Lab AC LCS M&C-Lab NK NSG DS X EDA/L MP/L FO&R/L EDC/L	D3 EC A D3 EC B1 D3 EC B2 DCS DCS-Lab MWR-Lab AKS GPK CJ F-105 COMM/L MWR/L	-x-	AOCS BK G8	---
	14:30	D1ECB PPS lab T(PPS) COMP/L	D2 EC A1 D2 EC A2 D2 EC B1 D2 EC B2 STRW-Lab LCS AC M&C-Lab NK DS KPS X EDA/L PROJ/L G8 EDC/L	D3 EC A D3 EC B1 D3 EC B2 MWR DCS-Lab MWR-Lab SK GPK CJ F-105 COMM/L MWR/L	-x-	PROJECT MR FO&R/L	---
15:30	---	-x-	D3 EC A, D3 EC B Skill Enhancement	-x-		---	