

GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA
(Department of Electronics & Communication Engineering)

No.ECE/

Date:

NOTICE

Date sheet for 2nd Internal Test is as follows. The detailed seating plan will be displayed on the department notice board on 13.11.2019.

Date	Time	B.Tech (ECE)			M.Tech (ECE)	
		3 rd Sem.	5 th Sem.	7 th Sem.	1 st Sem.	3 rd Sem.
13.11.2019	10:30am to 12:00pm	S&S	DSP	OC	MCS	MMC
13.11.2019	2:30pm to 4:00pm	CA	IPR/TQM	E. MGT	RM	--
14.11.2019	10:30am to 12:00pm	MATHS-III	ACS	VLSID	CCN	*O.E
14.11.2019	2:30pm to 4:00pm	EVS	--	--	--	--
15.11.2019	10:30am to 12:00pm	NAS	MPI	WSN / M.COMP	VDA	--
15.11.2019	2:30pm to 4:00pm	ED	LIC	SC / COA	AC	--
18.11.2019	2:30pm to 4:00pm	DEc	AWP	--	SDTM	--

*** Note: M.Tech students consult your subject teacher for the syllabus, exam timing and seating plan.**

HOD (ECE)