			G6		
	Monday	Tuesday	Wednesday	Thursday	Friday
	D2ECA	D2ECA		D2ECA	D2ECA
08:30	KPS	SMM		NK	CJ
	CA L	EVS L		Inesday Thursday D2ECA NK SS L D2ECA RPS A-III L D2ECA NSG DE L D2ECA NSG DE L D2ECA NSG DE L D2ECA NSG DE L D2ECA D2ECA D2ECA D2ECA D2ECA D3ECA D4WC D3ECB2 BK OE-II) L D3ECB2	NAS L
	D2ECA	D2ECA	D2ECA	D2ECA	D2ECA
09:30	NSG	NK	SS(AP)	KPS	NSG
	Monday Tuesday Wednesday Thursday	DE L			
	D2ECA	D2ECA	D2ECA	D2ECA	D2ECA
10:30	HKA	KPS	CJ	NSG	HKA
	ED L	CA L	NAS L	DE L	ED L
	D2ECA	D2ECA	D2ECA	D2ECA	D2ECA
11:30	CJ	SS(AP)	NK	HKA	SMM
	NAS L	EM-III L	SS L	ED L	EVS L
12:30					
	D2ECA		D2564 D256D	D2564 D256D	D3ECA2
1:30	SS(AP)		·		DS
	EM-III L		` ′	, ,	DSP T
2:30			OB L	COLL	
	D4WC	D.456A D.456D	DANAG	D256D2	DANAG
2 20	HK	•			D4WC
3:30	WC(OE-II)				HK
	L	MC P	WC(OE-II) L	MC P	WC(OE-II) L
	Timet	table generated	with FET 6.1.6	on 9/30/21 10:	53 AM

	G 7					
	Monday	Tuesday	Wednesday	Thursday	Friday	
	D2ECB	D2ECB	D2ECB		D2ECB	
08:30	SMM	NSG	KPS		HKA	
	EVS L	DE L	CA L		ED L	
	D2ECB	D2ECB	D2ECB	D2ECB	D2ECB	
09:30	CJ	SS(AP)	NK	HKA	KPS	
	NAS L	EM-III L	SS L	ED L	CA L	
	D2ECB	D2ECB	D2ECB	D2ECB	D2ECB	
10:30	NK	HKA	NSG	CJ	NK	
	SS L	ED L	DE L	NAS L	SS L	
	D2ECB	D2ECB	D2ECB	D2ECB	D2ECB	
11:30	NSG	CJ	SS(AP)	SMM	SS(AP)	
	DE L	NAS L	EM-III L	EVS L	EM-III L	
12:30						
		D2ECB				
1:30		KPS				
		CA L				
2:30						
			D2ECB1			
3:30			NSG			
			DE T			
	Timetable	generated	with FET 6.1.6	on 9/30/21	10:53 AM	

			G8		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30	M1EC BK CCNC(E-2) L	M1EC HK RM L		M1EC HSG SoftCom (E-1) L	
09:30		M1EC HSG SoftCom (E-1) L		M1EC BK CCNC(E-2) L	
10:30		M1EC MR MCS L	M1EC GPK VDA L		
11:30	M1EC HK RM L	M1EC GPK VDA L			
12:30					
1:30		D3ECB1 DS DSP T	M1EC BK CCNC(E-2) L		M1EC HK RM L
2:30	M1EC HSG SoftCom (E-1) L		M1EC MR MCS L	M1EC GPK VDA L	M1EC MR MCS L
3:30		D2ECB2 MR MC P	D3ECA1 DS DSP T		
	Timet	able generated v	with FET 6.1.6	on 9/30/21 10:5	3 AM

	F-105					
	Monday	Tuesday	Wednesday	Thursday	Friday	
	D3ECA		D3ECA	D3ECA	D3ECA	
08:30	KDS		DS	GJK	SK	
	CN L		DSP L	MPMC L	AWP L	
	D3ECA	D3ECA	D3ECA	D3ECA	D3ECA	
09:30	SK	DS	GJK	SK	GJK	
	AWP L	DSP L	MPMC L	AWP L	MPMC L	
	D3ECA	D3ECA	D3ECA	D3ECA	D3ECA	
10:30	DS	AKS	KDS	AKS	KDS	
	DSP L	ACS L	CN L	ACS L	CN L	
11:30						
			D3ECA			
12:30			AKS			
			ACS L			
					D3ECA1	
1:30					GJK	
					MPMCT	
2:30						
				D3ECB1		
3:30				KPS		
				MC P		
	Timetable	generated	with FET 6.1.6	on 9/30/21	10:53 AM	

	F-106					
	Monday	Tuesday	Wednesday	Thursday	Friday	
	D3ECB	D3ECB	D3ECB	D3ECB	D3ECB	
08:30	DS	GJK	GPK	MR	KDS	
	DSP L	MPMC L	ACS L	AWP L	CN L	
	D3ECB	D3ECB	D3ECB	D3ECB	D3ECB	
09:30	GJK	GPK	MR	KDS	DS	
	MPMC L	ACS L	AWP L	CN L	DSP L	
	D3ECB	D3ECB	D3ECB	D3ECB	D3ECB	
10:30	MR	KDS	DS	GJK	GPK	
	AWP L	CN L	DSP L	MPMC L	ACS L	
11:30	-X-	-X-	-X-	-X-	-X-	
12:30	-X-	-X-	-X-	-X-	-X-	
1:30	-X-	-X-	-X-	-X-	-X-	
2:30	-X-	-X-	-X-	-X-	-X-	
3:30	-X-	-X-	-X-	-X-	-X-	
	Timetable	generated	with FET 6.1.6	on 9/30/21	10:53 AM	

		MW/L					
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30							
09:30							
10:30			D4ECA2 KPS VLSI(E-IV) L				
11:30	D4ECA2 KPS VLSI(E-IV) L				D4ECA2 KPS VLSI(E-IV) L		
12:30							
1:30	D4ECA1 SMM ON (E-III) T	D2ECA2 BK, SMM	D2ECB1 HK		D2ECA2 NSG DE T		
2:30	DOECA1	DE-Lab P	DE-Lab P	Darcha			
3:30	D2ECA1 BK, SMM DE-Lab P	D1EC3 HSG MC P	D2ECA2 AKS MC P	D2ECB2 HK DE-Lab P			
	Timetable	generated	with FET 6.1.6	on 9/30/21	10:53 AM		

	PROJ/L						
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30		D4ECA GPK, CJ, HKA M.Proj P		D4ECA BK, HK, SK, SMM M.Proj P			
09:30							
10:30							
11:30							
12:30				D3ECB2 DS DSP T			
1:30			D2ECA2 NK SS T	D4ECA GJK, DS, SMM M.Proj P	D2ECA1 NK SS T		
2:30	D4ECA MR, NK, KDS M.Proj P	D4ECA AKS, HK, HSG M.Proj P		D2ECB1 NK SS T	D4ECA BK, HK, SK M.Proj P		
3:30		D1EC4 SMM MC P	D2ECB2 NK SS T		D2ECB2 NSG DE T		
	Timeta	able generated	with FET 6.1	.6 on 9/30/21 10:	53 AM		

			MP/L		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30					
09:30					
			D4ECA1		
10:30			BK		
			MC(E-IV) L		
	D4ECA1				D4ECA1
11:30	BK				BK
	MC(E-IV) L				MC(E-IV) L
12:30	D3ECA1				Darcha
	GJK	D3ECB2			D3ECB2
1:30	MPMC-Lab	GJK			MPMC-Lab P
	Р	MPMCT			IVIF IVIC-Lab F
			D4ECA1		
2:30	D3ECB1		BK	DAECAS	Dacas
	CJ		MC(E-IV) T	D4ECA2 MR	D3ECA2 GJK
	MPMC-Lab	D1EC2		SRTECE P	MPMC-Lab P
3:30	Р	SK		JIVILCE F	IVII IVIC-Lab P
		MCP			
	Timetable	generated	with FET 6.1.	6 on 9/30/2	1 10:53 AM

	COMM/L						
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30							
09:30							
10:30							
11:30	M2EC AKS MMC(E-5) L				M2EC AKS MMC(E-5) L		
12:30	D3ECB2 GPK			D3ECB1 GJK MPMC T	D3ECB1 GPK		
1:30	ACS-Lab P				ACS-Lab P		
2:30	D3ECA2 AKS, KPS ACS-Lab P		M2EC AKS MMC(E-5) L		D3ECA1		
3:30			D3ECA2 GJK MPMC T	D3ECB3 HSG MC P	AKS, KPS ACS-Lab P		
	Timetable	generated	with FET 6.1.6	on 9/30/21	10:53 AM		

	FO&R/L						
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30			D4ECA1		M1EC		
09:30	M1EC	M2EC	HK		GPK		
09.50	GPK	BK	ON-Lab(E-III) P		VDA-Lab P		
10:30	VDA-Lab P	Pre-Thesis P		M1EC	M1EC		
11.20			M2EC	MR, HSG	MR, HSG		
11:30		MR	MR	MCS-Lab P	MCS-Lab P		
12:30			Pre-Thesis P				
1:30							
2:30							
				D3ECA3			
3:30				KDS			
				MC P			
	Timetab	le generated	with FET 6.1.6 o	n 9/30/21 1	0:53 AM		

	EDA/L							
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30			-X-					
09:30			-X-					
10:30	D4ECA2 HSG MOSFET(E- III) L							
11:30			D4ECA2 HSG MOSFET(E-III) L					
12:30	D2FCD1	Dacas						
1:30	D3ECB1 NK DSP-Lab P	D3ECA2 SK DSP-Lab P			D4ECA2 HSG MOSFET(E-III) L			
2:30	D3ECA1		D4ECA2 KPS VLSI(E-IV) T		D3ECB2			
3:30	SK DSP-Lab P			D3ECA1 NK MC P	NK DSP-Lab P			
	Timeta	ble generate	ed with FET 6.1.6	on 9/30/2	I 10:53 AM			

			EDC/L		
	Monday	Tuesday	Wednesday	Thursday	Friday
08:30					
09:30					
	D4ECA1				
10:30	SMM				
	on (e-III) l				
			D4ECA1		
11:30			SMM		
			ON (E-III) L		
12:30					
	D4ECA2			D2ECA1	D4ECA1
1:30	HSG	D2ECA1	D2ECB2	NSG	SMM
1.50	MOSFET(E-	HKA	HKA	DE T	ON (E-III) L
	III) T	ED-Lab P	ED-Lab P	DLI	ON (L III) L
2:30	D2ECA2			D4ECA1	D2ECB1
	HKA	D2ECB1	D2ECA3	AKS	HKA
3:30	ED-Lab P	CJ	HKA	SRTECE P	ED-Lab P
	LD-Lab F	MC P	MC P	JINI LCL F	
	Timetable	generated	with FET 6.1.6	on 9/30/21	10:53 AM

	COMP/L							
	Monday	Tuesday	Wednesday	Thursday	Friday			
08:30			D4ECA2					
09:30			HSG MOSFET-Lab(E-III) P					
10:30								
11:30								
12:30	D3ECA2	D3ECA1						
1:30	KDS CN-Lab P	KDS CN-Lab P						
2:30	Darcha				D2FCD1			
3:30	D3ECB2 DS CN-Lab P	D1EC1 DS MC P	D2ECA1 GPK MC P	D3ECA2 SMM MC P	D3ECB1 KDS CN-Lab P			
	Timetable generated with FET 6.1.6 on 9/30/21 10:53 AM							

	TI/L						
	Monday	Tuesday	Wednesday	Thursday	Friday		
08:30							
09:30							
10:30							
11:30							
12:30							
			D2ECA1	D2ECA2			
1:30			CJ	CJ			
			NAS T	NAS T			
					D2ECB2		
2:30					CJ		
					NAS T		
		D1EC5		D2ECB1			
3:30		KPS		CJ			
		MC P		NAS T			
	Timetable generated with FET 6.1.6 on 9/30/21 10:53 AM						