


1	Name	Dr. SANDEEP SINGH GILL 
2	Designation:	Professor and Head (ECE)
3	Department	Electronics and Communication Engineering
4	Date of Joining	1st May 1998
5	Qualifications:	1. Ph.D (Electronics and Communication Engineering) [NIT, Hamirpur] 2. M.Tech(Electronics & Communication Engineering) [GNDEC, Ludhiana 2005] 3. MBA(Marketing /Systems & Control) [PAU, Ludhiana 1994] 4. B.E(Electronics & Comm. Engineering) [Punjab university, Chandigarh 1991]
6	Experience	1. Head of Department (May,2014 onwards) 2. Professor (18th February, 2013 onwards) 3. Associate/ Assistant Prof.(G.N.D.E.C., Ludhiana) [16th August, 2005 onward] 4. Lecturer (G.N.D.E.C., Ludhiana) [1st May 1998 till 15th August 2005] 5. Centre Manager(Tulec Computer Education, Khanna) [March 1996-April 1998] 6. Senior Executive(Wipro Ltd., Jalna Road, Aurangabad) [Jan 1994 to Dec. 1995]
7	Paper published	National Journals: 03 International Journals: 48
8	Paper presented in conferences	Conferences: 110
9	PhDs/ M.Tech thesis guided	Master's: 95 PhD: 06(Guiding)
10	Books published/IPRs./ Patents	1. Contributed chapter on “ Management information system” in Handbook of Management, Pragati Prakashan, 2005. 2. Contributed essay on “Marketing in 21stcentury”, in book

		<p>“Business Management- Parameters & Perspectives”, edited by Dr. Ajay Prasher & Rajeev Prasher, Medallion Press,2006.</p> <p>3. Proceedings, Recent Advances in Electronics & Communication Technologies, 2011. ISBN 978-93-8069772-7</p> <p>4. Proceedings, Recent Advances in Electronics & Communication Technologies,2013. ISBN 978-3-81-923777-4.2</p> <p>5. Book “CMOS SRAM Design and Analysis of Low leakage and High Speed SRAM Cell” co-authored by Navneet Kaur and Rohin Gupta published by Lambert Academic Publishing, 2016, ISBN - 978-3-659-86111-6.</p> <p>6. Proceedings, Soft Computing Applications in Wireless Communication SCAWC,2017</p> <p>7. Patents: 01 (Filed in 2016) “Methods And Systems For Resource Allocation In Cognitive Radio Networks”</p>
11	Professional Memberships	Fellow, Institution of Electronics & Telecommunication Engineers(IETE) Fellow, Institution of Engineers (India) Member, IEEE Member, Indian Society for Technical Education (ISTE) Member, Ludhiana Management Association
12	Area of Specialization/ Consultancy Activities	VLSI Design Automation, Soft Computing and CAD Tools Product & Quality management
13	Major Grant Fetched	<ol style="list-style-type: none"> 1. Opto electronics and fiber optics projects (TAPTEC) 2. Upgradation of EDA Lab (MODROBS) 3. DST, PTU, CSIR Grants for conduct for conferences etc. 4. As Departmental Coordinator Consultancy conducted 6 weeks courses on “Circuit design using VHDL” and generated resources through training students of other colleges and AMIE. 5. AICTE/ISTE sponsored FDP on “ Circuit Design using VHDL” , 2003 6. AICTE sponsored FDP on Advanced VLSI Design Techniques, 2014 7. AICTE sponsored FDP on Design Challenges in Low Power VLSI Design, 2017 8. AICTE sponsored seminar on Design issues in Low Power VLSI Domain, October 2017 9. MODROBS Grant by AICTE for the project ”RF Microwave Engineering” 2017 10. One week QIP STC on “ Design issues in low power electronics” 2018
14	Interaction with Professional institutions	<ol style="list-style-type: none"> 1. As expert in selection committees, conferences, STTPS, and in collaborated research. 2. Member Board of studies- GNDU Amritsar, & P.T.U Kapurthala 3. Reviewer in reputed journals and conferences. 4. Examiner at Ph.D / PG Level